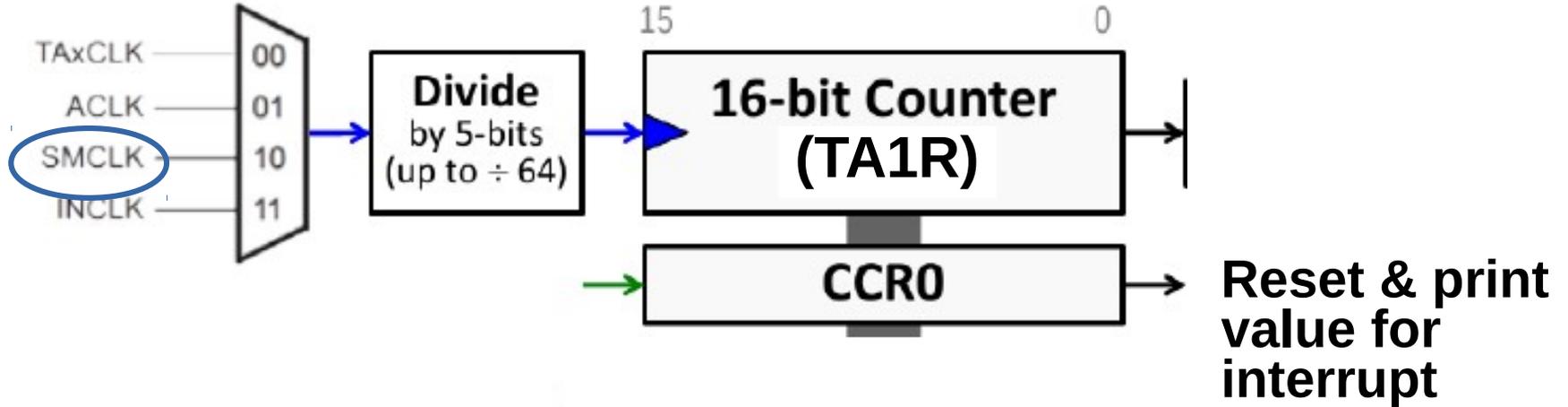
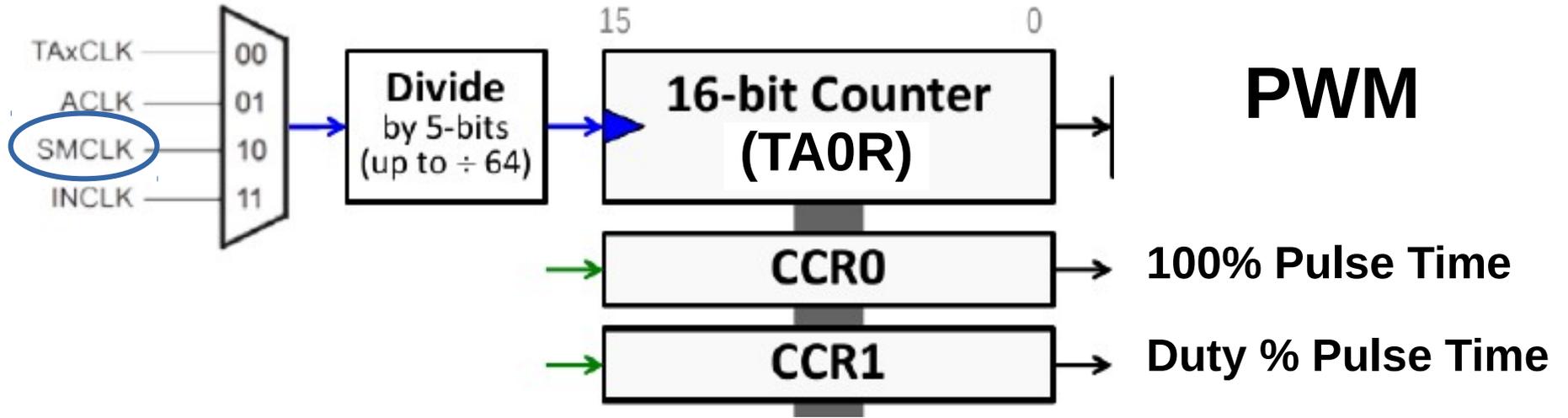


M7 Assignment

Generate PWM signal to fade the GREEN LED

And also use the Serial Plotter to plot the intensity values



12.2.5.1.1 Output Example—Timer in Up Mode

The OUTn signal is changed when the timer counts up to the TAxCCRn value and rolls from TAxCCR0 to zero, depending on the output mode. Figure 12-12 shows an example using TAxCCR0 and TAxCCR1.

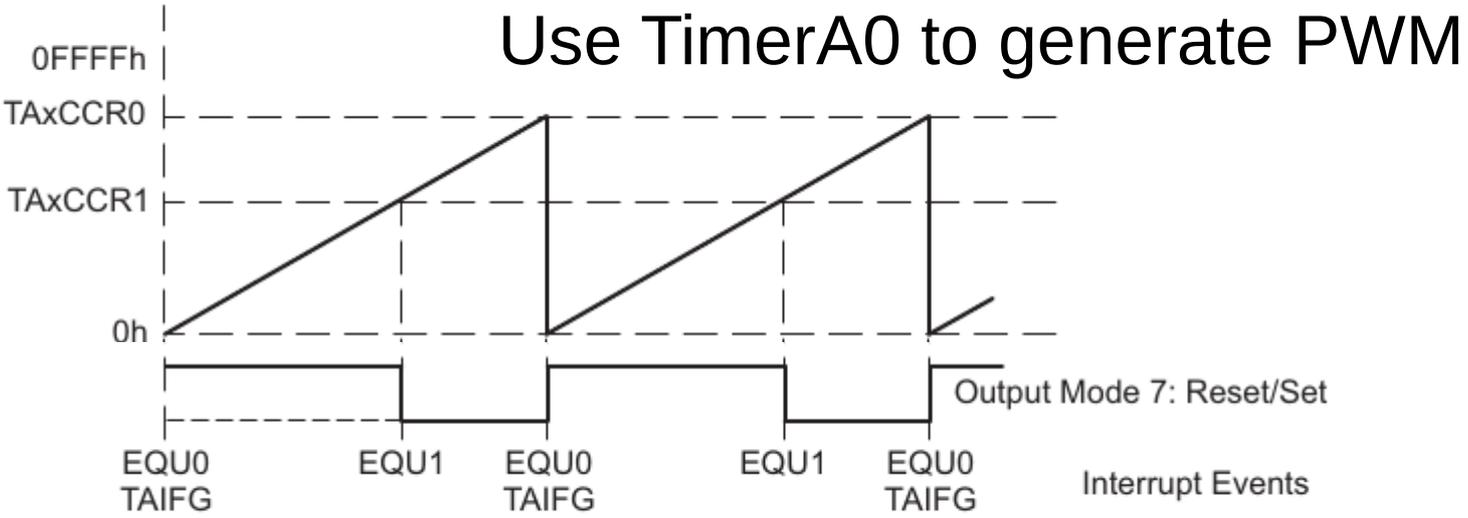


Figure 12-12. Output Example – Timer in Up Mode

```

54  /*** Timer0_A Set-Up ***/
55  TA0CCR0 |= 1000;           // PWM period
56  TA0CCR1 |= 1;             // TA0CCR1 PWM duty cycle
57  TA0CCTL1 |= OUTMOD_7;    // TA0CCR1 output mode = reset/set
58  TA0CTL |= TASSEL_2 + MC_1; // SMCLK, Up Mode (Counts to TA0CCR0)
59

```

NO INTERRUPT

SET TA0CCR1 to a fixed duty cycle – CCR1/CCR0

Use TimerA1 to interrupt every 4000 clock cycles to insert a new fixed duty cycle in TimerA0

```
60  /*** Timer1_A Set-Up ***/  
61  TA1CCR0 |= 4000;           // Counter value  
62  TA1CTL0 |= CCIE;         // Enable Timer1_A interrupts  
63  TA1CTL |= TASSEL_2 + MC_1; // SMCLK, Up Mode (Counts to TA1CCR0)  
64
```

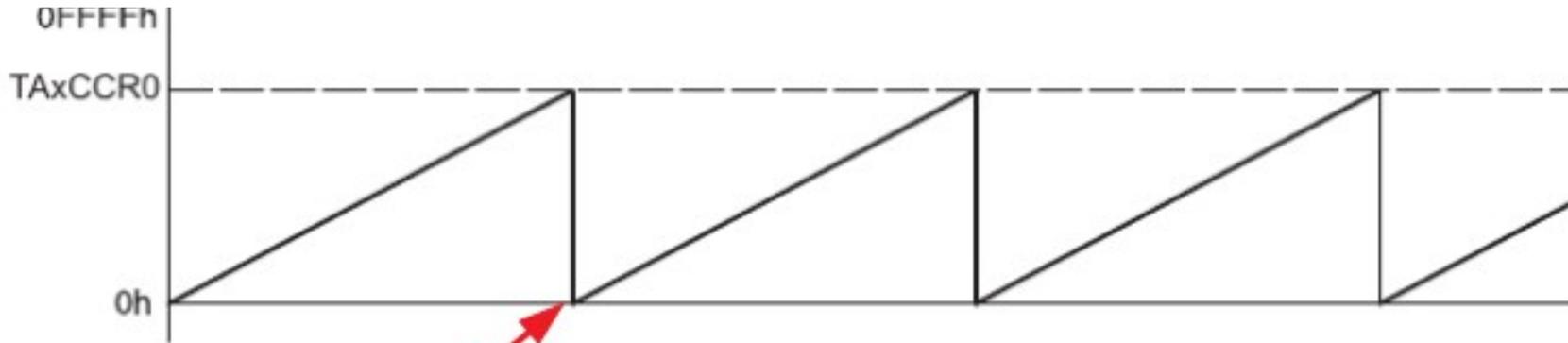
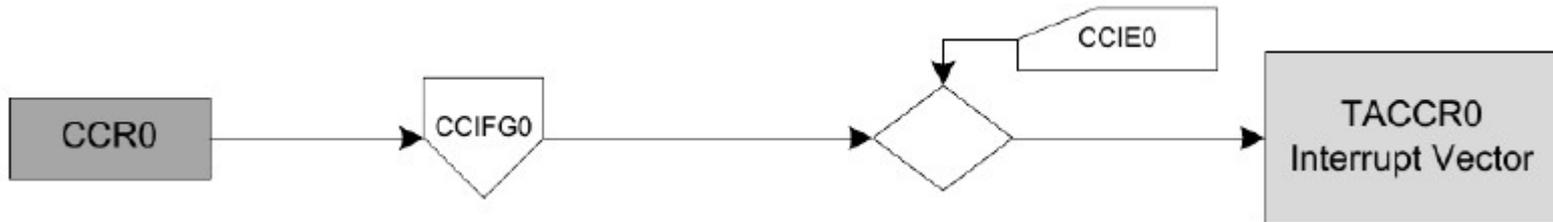


Figure 12-2. Up Mode



```

65
66   _BIS_SR(LPM0_bits + GIE);           // Enter Low power mode 0 with interrupts enabled
67 }

```

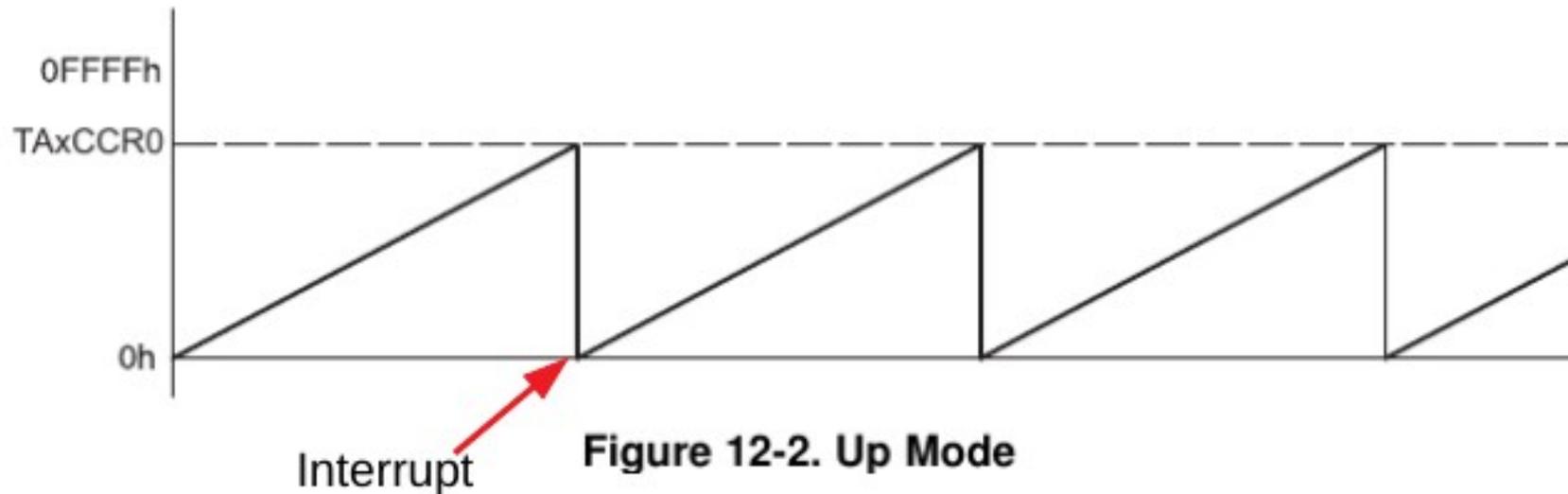


Figure 12-2. Up Mode

```

--
69 #pragma vector=TIMER1_A0_VECTOR           // Timer1 A0 interrupt service routine
70 __interrupt void Timer1_A0 (void) {
71
72     TA0CCR1 += IncDec_PWM;                // Increase or decrease on time
73     if( TA0CCR1 > 998 || TA0CCR1 < 2 ) // Reverse direction if it falls within values
74         IncDec_PWM = -IncDec_PWM;
75

```

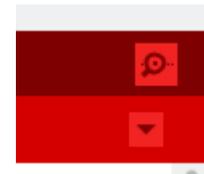
```

11 #include <stdio.h>          // need for Printf()
12
13 int putchar(int TxByte);    // need for Printf()
14 void UARTSetup (void);

--
87 void UARTSetup (void)
88 {
89
90     // Configure UART pins
91     P1SEL0 |= BIT4 | BIT5;          // set 2-UART pin as second function
92
93     // Configure UART
94     UCA0CTLW0 |= UCSWRST;
95     UCA0CTLW0 |= UCSSEL__SMCLK;
96
97     UCA0BR0 = 104;          // 1MHz SMCLK/9600 BAUD
98     // UCA0BR1 = 0x00;
99     UCA0MCTLW = 0x1100; // | UCOS16 | UCBRF_1;
100
101     UCA0CTLW0 &= ~UCSWRST;
102 }
103
104 int putchar(int TxByte)
105 {
106     while(!(UCA0IFG&UCTXIFG));
107     UCA0TXBUF = TxByte;
108     return 1;
109 }
110

```

```
68
69 #pragma vector=TIMER1_A0_VECTOR // Timer1 A0 interrupt service routine
70 __interrupt void Timer1_A0 (void) {
71
72     TA0CCR1 += IncDec_PWM; // Increase or decrease on time
73     if( TA0CCR1 > 998 || TA0CCR1 < 2 ) // Reverse direction if it falls within values
74         IncDec_PWM = -IncDec_PWM;
75
76     // print every 10th value
77     if (!DeciMate--)
78     {
79         DeciMate=10;
80         printf("%d\n", TA0CCR1);
81     }
```



```
/dev/ttyACM2
859
881
903
925
947
969
991
985
963
941
919
897
875
853
831
809
787
```

