

Electrical and Computer Engineering Florida International University Fall 2009

#### Performance

- What we care most about the computer performance
  - How fast it can run a program
  - Response time or throughput
  - Response time: time to finish one single program
     Throughput: total amount of work done in unit time

# **CPU Performance Equation**







### Improve CPU time

- Instruction count

   ISA and compiler technology
- CPI
- Organization and ISA
- Clock cycle time
  - Hardware technology and organization

# Speed measurement Speedup n: $m = \frac{\text{execution time of } Y}{\text{execution time of } X} = \frac{1/\text{performance } Y}{1/\text{performance } X} = \frac{\text{performance of } X}{\text{performance of } Y}$ X performs n times better than Y

<ul> <li>400-mhz processor</li> <li>2 million instructions</li> <li>CPU Time?</li> <li>Instruction Type CPI Instruction Mix         <ul> <li>ALU</li> <li>1</li> <li>60%</li> <li>Load/Store with 2</li> <li>18%</li> <li>cache hit</li> <li>Load/store with 8</li> <li>Branch</li> <li>4</li> <li>12%</li> </ul> </li> </ul>	Exa	ample			
Instruction TypeCPIInstruction MixALU160%Load/Store with cache hit218%Load/store with cache miss810%Branch412%	• 4 • 2 • C	00-mhz proc million instru PU Time?	essor uctions		
ALU     1     60%       Load/Store with cache hit     2     18%       Load/store with cache miss     8     10%       Branch     4     12%		Instruction Type	СРІ	Instruction Mix	
Load/Store with cache hit218%Load/store with cache miss810%Branch412%		ALU	1	60%	
Load/store with 8 10% cache miss Branch 4 12%		Load/Store with cache hit	2	18%	
Branch 4 12%		Load/store with cache miss	8	10%	
		Branch	4	12%	

### MIPS and MFLOPS

- MIPS (million instructions per second)
  - MIPS = IC/(CPU Time x 10<sup>6</sup>)
  - Problems?
    - High MIPS ≠ shorter CPU time
- MFLOPS (million floating point operations per second)
  - MFLOPS = floating point operations in a
  - program / (CPU Time x 10<sup>6</sup>)
  - Problems?





# A is 10x faster than B for Prog P1 B is 10x faster than B for Prog P2 B is 10x faster than A for Prog P2 A is 20x faster than A for Prog P1 C is 50x faster than C for Prog P1 B is 2x faster than C for Prog P1 C is 5x faster than B for Prog P2 B is 2x faster than B for Prog P3 C is 5x faster than B for Prog P4 C is 5x faster than B for Prog P5

	Computer 1	Computer 2	Computer 3
rogram A	1	10	20
	1000	100	20
otal	1001	110	40



Weight examp	ed arith le	meti	ic me	an	
	in (weighted)	1. 10 A	Comp A	Comp B	Comp C
n		Prog 1	1	10	20
$\sum$ weigh	ti * timei	Prog 2	1000	100	20
	W <sub>1</sub> =.5, W <sub>2</sub> =.5	W <sub>1</sub> =.90	9,W <sub>2</sub> =.091	W <sub>1</sub> =.999	9,W <sub>2</sub> =.001
Computer A	500.5	91	.909	1.	999
Computer B	55	1	8.10	10	0.09
Computer C	20		20		20



# Normalized Execution Time

Normalize to a particular machine by dividing all execution times by chosen machine's time
Example

Program P1 has the following execution times:

On machine A: 10 secs

On machine B: 100 secs

On machine C: 150 secs

Normalized to A: A=1, B=10, C=15 Normalized to B: A=.1, B=1, C=1.5 Execution time ratio

# Normalized Mean

Taking the average of the normalized times

rmalized geometric mean

 $\int_{n}^{n} \prod_{i=1}^{n} Execution time ratio_{i}$ 

					Com	рΑ	Cor	mp B	Com	рC	
	15 an 198		Pro	g 1	1		-	10	20	)	
			Pro	g 2	10	00	1	00	20	)	
r <mark>R (Exec</mark> ut	ion tii										
	No	malize	to A No		rmaliz	ed to	в	Nor	malized	to C	
100	А	в	С	Α	В		С	А	В	С	
ETR P1	1	10	20	0.1	1		2	.05	.5	1	
ETR P2	1	.1	.02	10	1		.2	50	5	1	
IGM (Norm	alized	l geon	netric i	mean	I)						
	No	Normalized to A		ed to A		Normalize	d to B	Norr	nalized	to C	
	A	В	С		Α	В		С	Α	В	C
NGM	1		.6	3	1			.63	1.58	1.58	1



# Amdahl's Law

Improvement by the faster mode is limited by the fraction of time the faster mode can be used

- Execution time of any code has two portions
  - $C_{total} = C_{p1} + C_{p2}$ 
    - $C_{p1} = (1-\alpha) * C_{total}$ : not affected by enhancement  $C_{p2} = \alpha * C_{total}$ : affected by enhancement
- Let n be the speedup factor for  $C_{p2}$ , then
  - $C_{new} = C_{p1} + C_{p2}/n = ((1-\alpha) + \alpha/n) * C_{total}$
- As n -> infinity,  $C_{new} \rightarrow (1-\alpha) * C_{total}$



### Example

- Enhancement: Vector mode
- Portions of code containing computations run 20x faster in vector mode.
- What % of original code must be vectorizable to achieve speedup<sub>overall</sub> = 2?

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 $\alpha = .5263$ 

 $Speedup_{overall} = \frac{execution time_{odd}}{execution time_{new}}$ 

 $=\overline{(1-\alpha)+\frac{\alpha}{20}}$ 

### Example

- FP operations =25%
- FP operation AVG CPI = 4.0
- AVG CPI for others = 1.33
- FP operatios for FPSQR = 2%
- CPI of FPSQR = 20
- Design 1: decrease the CPI of FPSQR to 2
- Deisgn 2: decrease average CPI of all FP to 2.5.
- Which one is better?

# Summary

- Measure performance

   Execution time/throughput
   CPU time
- Fair comparison of performance
   Weighted mean,
   normalization, geometric mean
   Principles in architecture design
   Amdahl's law