# Runtime Monitoring of ML-Based Scheduling Algorithms Toward Robust Domain-Specific SoCs

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Abstract—Machine learning (ML) algorithms are being rapidly 2 adopted to perform dynamic resource management tasks in 3 heterogeneous system on chips. For example, ML-based task 4 schedulers can make quick, high-quality decisions at runtime. 5 Like any ML model, these offline-trained policies depend crit-6 ically on the representative power of the training data. Hence, 7 their performance may diminish or even catastrophically fail 8 under unknown workloads, especially new applications. This 9 article proposes a novel framework to continuously monitor the 10 system to detect unforeseen scenarios using a gradient-based 11 generalization metric called coherence. The proposed framework 12 accurately determines whether the current policy generalizes to 13 new inputs. If not, it incrementally trains the ML scheduler 14 to ensure the robustness of the task-scheduling decisions. The 15 proposed framework is evaluated thoroughly with a domain-16 specific SoC and six real-world applications. It can detect whether 17 the trained scheduler generalizes to the current workload with 18 88.75%-98.39% accuracy. Furthermore, it enables 1.1x-14x <sup>19</sup> faster execution time when the scheduler is incrementally trained. 20 Finally, overhead analysis performed on an Nvidia Jetson Xavier 21 NX board shows that the proposed framework can run as a 22 real-time background task.

Index Terms—Domain-specific system on chip (SoC), imitation
 learning (IL), reinforcement learning (RL), resource manage ment, robustness, runtime monitoring, task scheduling.

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# I. INTRODUCTION

ETEROGENEOUS architectures integrate diverse computing elements, each tailored to optimize specific objectives, resulting in enhanced performance across various optimization fronts. Among these architectures, domain-specific systems on chip (SoCs) are meticulously designed to excel in particular domains, such as augmented/virtual reality, autonomous driving, and telecommunication [1], [2]. They maximize energy efficiency by integrating domain-specific hardware accelerators while supporting general-purpose computing by including general-purpose cores [3], [4], effectively blending

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Deployed Policy Incrementally Trained Policy ິ<u></u> New App Start Incremental Training 0.9 Line Execution 6.0 8x § 0.0 100 200 300 400 600 700 800 500 Time (ms)

Fig. 1. Illustration of incremental training. The gray dotted line represents the arrival of the unknown application, whereas the green line represents when the policy is updated with the incrementally trained version. The deployed and incrementally trained policies are IL-based scheduling algorithms. The execution time is  $8 \times$  lower after incremental training.

adaptability and efficiency. In the context of scheduling, the NPcomplete nature of the task scheduling problem poses significant challenges to traditional algorithms as the number of processing elements (PEs) and tasks increase due to the concurrent execution of multiple applications [5], [6]. This challenge has recently led researchers to develop machine learning (ML)based task scheduling and other dynamic resource management (DRM) techniques [7], [8], [9], [10], [11], [12].

ML-based policies can deliver fast and high-quality deci-45 sions tailored to a particular domain by leveraging system, 46 application, and task information as features. They are trained 47 using diverse workloads representing a target domain to 48 achieve this objective. Like any ML model, ML-based sched-49 ulers operate reliably within the confines of the datasets and 50 applications used during training. Consequently, they may fail, 51 or their performance may deteriorate when faced with new 52 workload scenarios, especially those involving new applica-53 tions [13], [14], [15]. Therefore, there is a strong need to 54 monitor the scheduling decisions to detect nonrobust decisions. 55

Fig. 1 illustrates the variation in the execution time as an 56 ML policy schedules streaming tasks to the PEs in an SoC. 57 Initially, the SoC runs a mixture of applications that were 58 used while training the ML scheduler. An unknown application 59 replaces the previous mix at the instance marked by the gray 60 dotted line. The average execution time begins to increase 61 substantially after the unknown application arrives and con-62 verges to the execution time of the new application. A close 63 inspection of the decisions reveals that the scheduler makes 64 incorrect decisions. As a concrete example, it fails to recognize 65 that one of the tasks in the new application could utilize a 66 hardware accelerator PE. Due to the incorrect decisions, the 67 execution time is  $8 \times$  longer than a scheduler trained with this 68 new application could achieve. Indeed, if one could detect the 69 arrival of a new application class and incrementally train the 70

1937-4151 © 2024 IEEE. Personal use is permitted, but republication/redistribution requires IEEE permission. See https://www.ieee.org/publications/rights/index.html for more information. <sup>71</sup> scheduler, it could achieve significantly higher performance,
<sup>72</sup> as depicted by the green vertical line in Fig. 1. This example
<sup>73</sup> shows two crucial needs when an ML-based resource manager,
<sup>74</sup> such as a scheduler, is used in domain-specific SoCs. First,
<sup>75</sup> it must recognize the input changes (e.g., the arrival of a
<sup>76</sup> new application) to which the scheduler does not generalize.
<sup>77</sup> Second, an on-the-fly incremental training technique must
<sup>78</sup> adapt the scheduler to changes in data distribution over time
<sup>79</sup> while retaining knowledge from past data.

This article proposes a novel framework that achieves the 80 81 following goals: 1) it monitors the actions of an ML-based 82 scheduler; 2) it detects the input changes that deviate from <sup>83</sup> the training data; and 3) it incrementally trains the ML 84 policy to adapt to the new application. To present a concrete <sup>85</sup> implementation of the proposed framework, we employ two 86 runtime task schedulers, one trained using imitation learning 87 (IL) and the other with reinforcement learning (RL). The <sup>88</sup> proposed runtime monitoring is performed as a background <sup>89</sup> task while an ML scheduler assigns incoming tasks to the 90 PEs in the SoC. It first reads the features used by the ML <sup>91</sup> scheduler, such as expected task execution times and PE 92 states. Then, it computes the gradient of the trained ML 93 policy and a coherence value using the gradient. When the 94 gradient of the trained policy and information added by the 95 new data samples are aligned, the coherence value is low, <sup>96</sup> indicating that the current model generalizes well to the latest 97 data samples. In contrast, when the latest data samples are <sup>98</sup> not aligned with training, the coherence increases, indicating 99 the need for retraining. When this happens, the proposed <sup>100</sup> framework incrementally updates the ML policy, adapting it <sup>101</sup> to new applications while retaining past information.

The efficacy of the proposed framework is assessed using 102 103 six real-world communication and radio frequency (RF) appli-104 cations running on a domain-specific SoC with sixteen PEs, 105 including general-purpose big core clusters alongside fixed-<sup>106</sup> point accelerators. Two instances of the proposed framework 107 tailored to IL and RL schedulers monitor the SoC while a <sup>108</sup> subset of the domain applications are launched. The proposed 109 framework determines whether the IL scheduler generalizes to 110 incoming data with over 98% accuracy. It misses only 0.59% 111 of data points the scheduler fails to generalize. Furthermore, <sup>112</sup> incrementally training the scheduler enables, on average,  $_{113}$  4.21× faster execution time. The detection accuracy drops 114 to 88.75% while monitoring an RL scheduler since RL 115 policies rely on a reward function, a weaker feedback than 116 the reference label available in IL. The proposed framework 117 can still effectively flag when incremental training is needed 118 and enable, on average,  $1.32 \times$  faster execution. Finally, <sup>119</sup> we implemented the proposed monitoring framework on the 120 Nvidia Jetson Xavier NX board [16] to assess its runtime 121 overhead. Since the proposed framework is not on the crit-122 ical path, the execution time affects only 1) how fast poor 123 scheduling decisions can be detected and 2) how frequently 124 the monitoring process can be repeated. Even when all the 125 steps of the proposed framework run sequentially, the worst-126 case execution times of the IL and RL instances are 83.74 and 127 117.53 ms, respectively. Hence, they can be effectively used 128 as real-time background processes that run periodically, as <sup>129</sup> detailed in Section V.

The main contributions of this article are as follows.

 A framework that continuously monitors the system, <sup>131</sup> identifying unforeseen tasks and incrementally training <sup>132</sup> the model as needed. <sup>133</sup>

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- Integration of a coherence-based detection mechanism 134 within reinforcement and IL approaches. 135
- Comprehensive experiments showcasing the effectiveness of the proposed framework in restoring 137 performance.
- 4) Runtime overhead analysis with hardware 139 measurements. 140

The remainder of this article is organized as follows. <sup>141</sup> Sections II and III review the related work and present the <sup>142</sup> background on the coherence metric and ML schedulers. <sup>143</sup> Section IV describes the proposed framework and its applica-<sup>144</sup> tion to IL/RL schedulers. Section V presents comprehensive <sup>145</sup> experimental evaluations and hardware measurements. Finally, <sup>146</sup> Section VI summarizes our conclusions. <sup>147</sup>

#### II. RELATED WORK

Domain-specific SoCs have gained traction in recent years 149 following the demand for specialized processing and energy- 150 efficient solutions. Recent work discussed these architectures 151 and proposed accelerations frameworks across different application domains [17], [18], [19], [20]. Modern computing 153 systems, including domain-specific SoCs, often rely on run- 154 time heuristics for task scheduling [21], [22], [23]. Alongside 155 heuristic schedulers, list-based schedulers [24], [25], [26], 156 [27], [28], [29] have been proposed for task scheduling, aiming 157 to optimize performance metrics at design time. However, one 158 limitation of list-based schedulers is their inability to account 159 for scenarios involving multiple streaming applications with 160 varying initialization times. Furthermore, optimization-based 161 schedulers, such as those utilizing integer linear or constraint 162 programming techniques [30], [31], [32], aim for optimal 163 decision making but suffer from infeasible runtime overheads 164 due to computational complexity. 165

ML-based task schedulers have recently emerged as alter- 166 natives to conventional algorithms and heuristics [7], [8], [9], 167 [10], [11], [12], [33], [34], [35], [36], offering reduced over- 168 head while achieving near-optimal outcomes. They leverage 169 various features, including performance counters, task, and 170 application-related data, to make informed decisions. These 171 features encompass a broad spectrum of metrics, ranging from 172 task execution durations on diverse resources to communi- 173 cation latencies and resource availability, chosen strategically 174 to optimize decision making. At the same time, a deep 175 neural network or decision tree policy enables predictable 176 execution time and runtime overhead optimization. These 177 schedulers leverage various ML methods, such as support 178 vector machine (SVM) [36], IL [10], [11], and RL [7], [8], [9], 179 [33], [34], [35]. IL models, for instance, emulate the behaviors 180 of complex schedulers impractical for runtime usage, showcas- 181 ing efficiency by eliminating the need for exhaustive search or 182 optimization algorithms. However, they are prone to sensitivity 183 toward their training datasets and inherent biases from expert 184 behaviors, rendering them vulnerable to unseen changes and 185 generalization issues. In contrast, RL-based schedulers learn a 186

<sup>187</sup> policy that optimizes a performance metric [8], [9] or multiple <sup>188</sup> metrics [34] by exploration. For instance, Decima [7] spe-<sup>189</sup> cializes in cluster-level scheduling for streaming applications <sup>190</sup> using graph and deep neural networks. These schedulers also <sup>191</sup> allow runtime adaptability, iteratively refining their weights <sup>192</sup> to accommodate changes in response to evolving workload <sup>193</sup> dynamics. They provide a significant advantage over static <sup>194</sup> approaches, such as heuristics, particularly in swiftly changing <sup>195</sup> environments inherent to domain-specific SoCs. Nonetheless, <sup>196</sup> all these methods necessitate a monitoring framework to <sup>197</sup> 1) confirm the generalization of the ML policy to new data <sup>198</sup> encountered at runtime and 2) adapt the policies if needed.

Monitoring frameworks for ML models focus on detecting 199 <sup>200</sup> data and concept drifts. Data drift detection methods [37], [38] 201 typically employ statistical models to assess whether 202 the observed data deviate significantly from a reference 203 distribution. In contrast, concept drift detection meth-<sup>204</sup> ods [39], [40], [41] focus on detecting shifts in the relationship <sup>205</sup> between input and output using statistical and ML-based classifiers. However, these methods often have high computational 206 207 complexity and execution times in the order of seconds, making them impractical for runtime applications, especially 208 209 in scenarios with short task durations typical of domain-210 specific SoCs. Additionally, their ability to adapt to evolving <sup>211</sup> data distributions may be limited due to inherent assumptions. 212 In contrast, our proposed framework takes a different approach 213 by leveraging changes in gradients to quantify generalization 214 to new data without making assumptions about the input 215 data. Moreover, recent work has explored the robustness of <sup>216</sup> ML models using mixed-integer linear programming (ILP), 217 resulting in runtime requirements ranging from seconds to <sup>218</sup> minutes [42]. On the task scheduling problem, researchers 219 discuss the robustness of task scheduling methods [43], using 220 metrics, such as expected execution time and missed deadlines, 221 often overlooking considerations related to generalizing to 222 new applications. To the best of our knowledge, our proposed 223 framework is the first runtime monitoring framework tailored 224 for ML-based task scheduling on domain-specific SoCs.

#### 225 III. BACKGROUND ON COHERENCE AND ML SCHEDULERS

This section first introduces coherence and its implications for the generalization of ML policies. Then, it overviews the use of ML schedulers in the domain-specific SoC context and describes the schedulers used in this work.

### 230 A. Background on the Coherence

Deep learning models trained with gradient descent have shown promising results in various fields, often demonstrating impressive generalization capabilities on unseen data. However, recent work notes that these networks theoretically have the capacity to memorize the training data. So, they could fail on any new input data [44], [45], [46]. Indeed, studies have shown that training with even entirely random data can lead to good training accuracy. Still, the models fail to generalize and exhibit poor accuracy on new data, indicating memorization. Hence, it is crucial to understand how gradient descent and the training process find solutions

Fig. 2. Evolution of the coherence and accuracy throughout training. The examples exhibit stronger mutual support in the early epochs, resulting in higher coherence (the right *y*-axis). As training progresses, the expected gradient of samples approaches zero, indicating that the samples no longer provide significant assistance to one another. Consequently, coherence tends to diminish toward zero by the end of the training period.

that generalize well among all possible solutions that fit the <sup>242</sup> training data [45], [46]. <sup>243</sup>

One of the recent ongoing attempts to explain generalization <sup>244</sup> in deep learning is "Coherent Gradients" [44], [47]. The core <sup>245</sup> idea is that gradients calculated from similar training samples <sup>246</sup> should be coherent, meaning they point in similar directions, <sup>247</sup> allowing generalization (rather than memorization) to occur. <sup>248</sup> In other words, the theory suggests that the interaction and <sup>249</sup> reinforcement between gradients from different training examples lead the model to learn features that generalize well to <sup>251</sup> unseen data. <sup>252</sup>

Suppose z is a sample from a batch  $(\mathcal{M})$  with  $M = |\mathcal{M}|$  <sup>253</sup> data samples. Further, let  $l_z(w)$  denote the loss function for <sup>254</sup> this sample, where w represents the trainable parameters of <sup>255</sup> this model. One can compute the gradient for this sample as <sup>256</sup>  $g_z = [\nabla l_z](w)$ . Chatterjee and Zielinski [44] quantified the <sup>257</sup> coherence over these M samples using per-sample gradients. <sup>258</sup> Specifically, they refer to the similarity between per-sample <sup>259</sup> gradients as *coherence* and define it as <sup>260</sup>

$$\alpha_M = M \cdot \frac{\mathbb{E}_{z \sim \mathcal{M}} [g_z] \cdot \mathbb{E}_{z \sim \mathcal{M}} [g_z]}{\mathbb{E}_{z \sim \mathcal{M}} [g_z \cdot g_z]}.$$
 (1) 261

When the gradients  $(g_z)$  are perfectly aligned, the numerator and denominator will be equal, leading to maximum 263 coherence (M). When all samples are fit, coherence will 264 be zero, meaning the individual gradients will become 265 zero. 266

During the initial training epochs, training data often shares <sup>267</sup> many common features. This results in aligned gradients and, <sup>268</sup> consequently, a higher coherence value. As training progresses <sup>269</sup> and trainable parameters converge, new features become more <sup>270</sup> specific, and the model tries to learn them individually. <sup>271</sup> Consequently, the coherence value tends to decrease, as <sup>272</sup> illustrated in Fig. 2. <sup>273</sup>

Using Coherence for Runtime Monitoring: Gradients reinforce each other when learning takes place during the early 275 training phases, leading to high coherence, as shown in the 276 first few epochs of Fig. 2. After the model has learned what 277 is common to all the samples and the samples have been 278 fit (in a well-generalizing manner), the coherence drops and 279 stabilizes to a low value. When the workload falls within the 280 generalized set at runtime (not necessarily identical to the 281 training data), its behavior resembles the end of the training 282 <sup>283</sup> phase illustrated in Fig. 2. Consequently, it is characterized <sub>284</sub> by a low coherence value (like the latest data sample during 285 training. However, if the new data samples deviate from 286 the training data, their gradients would align, leading to a rise in the coherence value. Therefore, increasing coherence 287 <sup>288</sup> indicates that the model processes features from an application that it has not generalized yet. The coherence will remain 289 290 high unless the ML policy, e.g., the scheduling algorithm, is incrementally trained. We leverage this observation in the 291 292 proposed runtime monitoring framework. A low coherence 293 for generalized workload indicates good performance, while sustained high coherence suggests encountering new data 294 a <sup>295</sup> that requires retraining. Using a smaller sample size of Mwould result in a smaller overall coherence range [zero to M<sup>297</sup> in (1)], making the framework more susceptible to random 298 noise during inference and negatively impacting accuracy. In 299 contrast, a larger M would result in increased overhead, as 300 discussed in Section V-D (see Tables II and III).

While Chatterjee and Zielinski [44] and Chatterjee [47] focused only on neural network models, we observe that the generalization theory using coherence can be used with any ML policy trained with gradient descent. Hence, we applied this framework to two scheduling algorithms: IL and RL. The IL model is trained with neural networks, while the RL model is trained with differential decision trees (DDTs), as elaborated in the following sections.

# 309 B. ML-Based Scheduling for Domain-Specific SoCs

Domain-specific SoCs are designed to deliver high 310 311 performance when running applications from a target domain. 312 A defining characteristic of these applications is processing 313 streaming inputs for prolonged periods. For example, consider <sup>314</sup> a domain-specific SoC designed for telecommunication. When 315 the user starts the WiFi application, it processes received 316 frames or transmits new ones for minutes, if not hours. 317 Throughout this duration, the SoC continuously schedules the 318 tasks comprising the WiFi transmitter and receiver chains. We envision that the proposed framework can run when a 319 320 new application launches or periodically. The monitoring can 321 repeat in the order of seconds or slower since there is no 322 need to check the scheduler operations faster than that due 323 to application lifetimes. Notable approaches of ML schedulers 324 (IL- and RL-based training) are discussed next.

IL Schedulers: IL is an ML method where an agent learns 325 policy  $(\pi)$  that mimics the behavior of an expert  $(\pi^*)$ 326 a 327 using the expert's actions. IL aims to minimize the error 328 between the actions taken by the agent  $(a_t)$  and the expert  $a_{29}$   $(a_t^*)$ . The expert actions  $(a_t^*)$  are collected offline and paired with corresponding states  $(s_t, a_t^*)$  for the agent to learn a 330 policy  $(\pi_{\theta})$  [48]. However, this approach has limitations, as the 331 332 behavior of the expert confines the agent's policy. To address 333 this issue, the data aggregation (DAgger) algorithm [49] enhances the performance of IL by iteratively reinforcing 334 335 incorrect decision-state pairs into the training set, thereby <sup>336</sup> correcting deviations and improving overall performance.

In the context of task scheduling, IL-based models leverage offline training capabilities. For example, the training data is collected through executing various workloads under different <sup>339</sup> system states to cover low to high congestion. During this <sup>340</sup> process, an expert scheduler makes decisions for these workloads, with the data representing the system state  $(s_t)$  collected <sup>342</sup> alongside the expert's policy decisions  $(\pi^*(s_t))$ . These system <sup>343</sup> states and their corresponding action pairs are then utilized <sup>344</sup> as features and target labels for supervised learning methods <sup>345</sup> within the IL model. Subsequently, the learned IL policy  $(\pi_{\theta})$  <sup>346</sup> is deployed for runtime decision making, replacing the expert <sup>347</sup> policy  $(\pi^*)$ . The expert may be a sophisticated heuristic or <sup>348</sup> a constrained programming scheduler, which can make highquality decisions but with a significant overhead. <sup>350</sup>

*RL Schedulers:* Unlike IL schedulers, RL schedulers do not <sup>351</sup> require an expert scheduler to guide the policy toward optimal <sup>352</sup> behavior [8], [33], [34]. During training, the agent interacts <sup>353</sup> with the environment by taking action  $(a_t)$  based on the current <sup>354</sup> state  $(s_t)$ , such as expected task execution and earliest PE <sup>355</sup> availability times. For each action, the environment gives the <sup>356</sup> agent a reward  $(r_t)$  that reflects how well the action aligns with <sup>357</sup> the performance objectives, such as minimizing the execution <sup>358</sup>

RL training algorithms commonly use actor–critic architectures, where the actor selects the actions  $(a_t)$ , and the critic selects the actions  $(a_t)$ , and the critic selects the actor and critic selects their expected outcomes. Both the actor and critic selects are continuously updated based on the feedback from the senvironment in terms of reward, allowing the agent to refine its selects  $(\pi_{\theta})$  over time [50]. The agent aims to optimize policy  $(\pi_{\theta})$  that takes actions to maximize the total reward over time. See The state value function can be used to find expected rewards set starting from an initial state following the policy. This value set the total reward over time set ( $\phi$ ) that returns an expected value according to the state of the environment.

# IV. ROBUST MONITORING OF ML-BASED SCHEDULING 372 ALGORITHMS 373

This section describes the proposed robust monitoring <sup>374</sup> framework overviewed in Fig. 3. Section IV-A introduces the <sup>375</sup> runtime monitoring component of the framework for detecting <sup>376</sup> workload changes. Then, Sections IV-B and IV-C present <sup>377</sup> the application of the proposed framework to IL and RL <sup>378</sup> schedulers, respectively. Finally, Section IV-D discusses its <sup>379</sup> applicability to other ML-based dynamic runtime management <sup>380</sup> frameworks, and Section IV-E presents the incremental training approach used in our robust monitoring framework. <sup>382</sup>

#### A. Robust Detection of Workload Changes

The first step of the proposed robust monitoring framework is continuous monitoring to detect the variations in the workload that can lead to incorrect decisions, as shown in Fig. 3. It is implemented as a background process to avoid any performance impact. Suppose the scheduler takes an action at time  $T_0$ . The system runs as usual by committing this action without interrupting the operation. At the same time, the background monitoring process is invoked to evaluate the quality of this action after the task is completed, as illustrated in Fig. 4. This evaluation is performed by calling a reference



Fig. 3. Overview of the proposed framework that monitors the scheduler decisions and application features used for decision making. It is activated to compute the coherence of a batch with M samples. The primary steps are: 1) generating the reference scheduler action for IL or reward calculation for RL; 2) loss, gradient, and coherence calculations; and 3) an optional incremental training step triggered by the coherence value.



Fig. 4. Event diagram illustrating the proposed monitoring framework *for IL schedulers*. This figure shows the tasks in series for clarity, but multiple parallel tasks can be scheduled and monitored concurrently. While monitoring IL schedulers, a trustworthy (but slower) scheduler runs in the background to determine the correct action  $(a_t^*)$ . This reference and actual policy actions  $(a_t)$  for a batch with *M* tasks are used for the loss, gradient, and coherence calculations (detailed in Algorithm 1). The incremental training step is executed if the framework decides the IL model policy  $(\pi_{\theta})$  should be updated.

394 scheduler with identical inputs and finding the reference 395 action when monitoring an IL-based scheduler (detailed in Section IV-B). In the case of an RL-based scheduler, the 396 reward received for this action is used to assess its quality 397 (detailed in Section IV-C). Then, the outcome of this assess-398 ment is used to compute the gradient of the ML policy. Finally, 399 the gradient is used to compute the coherence, as described 400 in Section III-A. An insignificant change in the coherence 401 value shows that the current ML policy handles the monitored 402 403 application well. That is, the policy generalizes well to the <sup>404</sup> monitored application. In contrast, a rise in coherence indicates new directions in the gradient, signifying the need to adopt the 405 policy to address the changes in the workload. The specific 406 407 details of the coherence calculation for IL- and RL-based <sup>408</sup> schedulers are described in the following sections.

Background Process Overhead: The proposed monitoring 409 410 and detection framework is implemented as a background 411 process, as mentioned above and illustrated in Fig. 4. The 412 system moves on with the current scheduling decision to avoid 413 interruption since an incorrect decision only leads to transient 414 performance degradation but not catastrophic failure. Hence, 415 the proposed framework is not on the critical path. However, 416 its overhead is still crucial since it determines how frequently 417 the proposed framework can be called and the detection <sup>418</sup> speed. Our hardware measurements indicate that the proposed 419 monitoring and detection can be performed in the order 420 of milliseconds, allowing frequent checks for the robustness 421 of the ML policies. Given the types and composition of 422 applications running on SoCs do not change in the order of 423 seconds, the proposed framework enables runtime monitoring <sup>424</sup> with negligible overhead, as detailed in Section V-D.

#### B. Application to IL-Based Schedulers

This section outlines the runtime detection framework <sup>426</sup> employed for IL-based task scheduling frameworks. Fig. 4 <sup>427</sup> illustrates the calculation steps in runtime monitoring for <sup>428</sup> IL-based schedulers, while Algorithm 1 provides a detailed <sup>429</sup> breakdown of these steps in the runtime detection process. <sup>430</sup>

Once activated, the proposed monitoring framework  $_{431}$  processes the actions  $(a_t)$  taken by the policy  $(\pi_{\theta})$  for a  $_{432}$  sample size of *M* (Algorithm 1, lines 4 and 5). IL schedulers  $_{433}$  operate as supervised learning models, wherein an agent learns  $_{434}$ 

a policy  $(\pi_{\theta})$  from an expert's decision-making patterns to 435 guide runtime scheduling decisions by generating actions  $(a_t)$  436 (line 6). Therefore, the runtime detection framework requires 437 the reference targets  $(a_t^*)$  obtained by invoking the expert 438 scheduler and collecting necessary performance metrics in 439 the background to avoid execution time overhead (line 7). 440 In this work, we employ a resource-intensive heuristic, the 441 earliest task first (ETF) scheduler that loops through all ready 442 tasks and PEs to choose the task assignment that minimizes 443 the expected execution time [23]. The authors of the IL 444 scheduler select ETF as the reference scheduler because its 445 overhead grows quadratically, ranging from 0.3 to 8 ms. In 446 contrast, the IL scheduler overhead grows linearly and enables 447 nanosecond-level decisions [10], [34]. The reference actions 448 are used to compute the loss function, denoted as  $\mathcal{L}_{\theta}$  (line 8), 449 in conjunction with the IL policy actions. We utilize the cross- 450 entropy loss for  $\mathcal{L}_{\theta}$ . Then, the loss function is used to calculate <sup>451</sup> the gradients  $g_z$ . Subsequently, we calculate the expected value  $_{452}$ of the gradient vector,  $\mathbb{E}_{z\sim \mathcal{M}}[g_z]$ , by adding the gradient 453 vectors and  $\mathbb{E}_{z \sim \mathcal{M}}[g_z \cdot g_z]$  by adding the dot product of the 454

Fig. 5. Event diagram illustrating the proposed monitoring framework for RL schedulers. As in Fig. 4, the tasks are shown in series for clarity, but multiple parallel tasks can be scheduled and monitored concurrently. The proposed framework performs the loss calculation using the estimated value function  $(V_{\phi}(s_t))$ from the critic network and rewards  $(r_t)$  from completed tasks. Then, the gradient is calculated for mini-batches (lines 13–17 in Algorithm 2), while coherence is calculated using batch coherence as given in line 19 in Algorithm 2. If the RL policy does not generalize to the current data points, it can be incrementally trained or turned off until the coherence reduces.

Algorithm 1 Detection Phase for the IL Scheduler

- 1: Input: Policy action set  $\mathcal{M}$  with M actions, Call period of the framework P
- 2: Output: Coherence value
- 3: **Initialization:** ML policy  $\pi_{\theta}$  with parameters  $\theta$ ,  $\mathbb{E}_{\mathcal{M}}[g_z] \coloneqq 0, \mathbb{E}_{z \sim \mathcal{M}}[g_z \cdot g_z] \coloneqq 0$
- 4: Call robust monitoring framework every P timeframe
- 5: while Total number of actions < M do
- Get policy action  $a_t$ 6:
- Get ground truth  $a_t^*$  in the background using reference 7: scheduler
- Calculate the loss function,  $\mathcal{L}_{\theta}$  using  $a_t^*$  and  $a_t$  as 8: CrossEntropyLoss $(a_t^*, a_t)$  [51]
- 9: end while
- 10: // gradients and coherence calculation
- 11: for sample from 1 to M do
- Calculate gradients  $(g_z)$  using loss function  $\mathcal{L}_{\theta}$  from 12: current sample
- Update estimate  $\mathbb{E}_{z \in \mathcal{M}}[g_z] := \mathbb{E}_{z \in \mathcal{M}}[g_z] + g_z$ 13:

14: Update estimate 
$$\sum_{z \sim \mathcal{M}}^{z \circ \mathcal{M}} [g_z \cdot g_z] \coloneqq \sum_{z \sim \mathcal{M}}^{z} [g_z \cdot g_z] + g_z \cdot g_z$$

15: end for

15: Coherence 
$$\alpha_M = M \cdot \frac{\sum\limits_{z \sim \mathcal{M}} [g_z] \cdot \sum\limits_{z \sim \mathcal{M}} [g_z]}{\sum\limits_{z \sim \mathcal{M}} [g_z \cdot g_z]}$$

455 gradient vectors of weights, respectively. This process can 456 be executed efficiently, with expected values computed using 457 running sums without storing the gradients, either incremen-458 tally or collectively, at each monitoring session's conclusion.  $_{459}$  Finally, the coherence is computed using (1). It determines the 460 coherence of gradients among all examples in the sample set  $\mathcal{M}$ , thereby detecting the unforeseen task scheduling scenarios 461 <sup>462</sup> that differ significantly from those encountered during training. We use the loss function for coherence instead of relying on 463 464 accuracy because the accuracy metric misses differences when 465 the policy generates the same actions with low confidence. 466 Besides, accuracy remains relatively stable when a few new 467 application instances are added to the workload mix. The loss <sup>468</sup> value, in contrast, is sensitive to such variations.

# C. Application to RL-Based Schedulers

This section presents the steps to apply our runtime monitor- 470 ing framework to RL schedulers. During monitoring, the actor 471 policy  $(\pi_{\theta})$  makes scheduling decisions (action  $a_t$ ) for new 472 tasks based on the SoC state ( $s_t$ ). The selected PEs process 473 the tasks as normal. As described in Section III-B, RL is 474 an unsupervised learning method where both the actor and 475 critic networks are trained during the offline training phase to 476 maximize the reward defined as the negative execution time. 477 Therefore, estimated state values  $(V_{\phi}(s_t))$  from the trained 478 critic network and rewards  $(r_t)$  expressed as the negative 479 of the task execution times are used to calculate the loss 480 function  $\mathcal{L}_{\theta}$  required for the gradient calculation. As new 481 tasks arrive, the trained critic network updates the state 482 values in the background, as illustrated in Fig. 5. Upon task 483 completion, rewards in terms of execution time are acquired 484 from the PEs. These rewards and the state values are used 485 to calculate the advantage function  $(A(s_t, a_t))$  for the state- 486 action pair, following the same equation as given in the PPO 487 algorithm [50]: 488

$$A(s_t, a_t) = r_t + \gamma V_{\phi}(s_{t+1}) - V_{\phi}(s_t)$$
(2) 489

where  $\gamma$  represents the discount factor and  $V_{\phi}(s_{t+1})$  is the 490 state value after completion of the task. The loss calculation 491 during training also uses the ratio between the updated policy 492 and the previous policy  $\rho(\theta)$ . Since the policy remains fixed 493 during inference at runtime, the probability ratio  $\rho(\theta)$  remains 494 equal to one. Thus, policy loss  $\mathcal{L}_{\theta}$  is given by the advantage 495 function in (2) and used in Algorithm 2 (line 10) 496

$$\mathcal{L}_{\theta} = \rho(\theta) \cdot A(s_t, a_t); \ \rho(\theta) = \frac{\pi_{\theta}(a_t|s_t)}{\pi_{\theta}old(a_t|s_t)} = 1.$$
(3) 497

Since this loss is not directly derived from the ground truth, 498 the resulting gradient and coherence become noisy. To address 499 this, we split the batch  $\mathcal{M}$  (with  $M = |\mathcal{M}|$  samples) into a 500 set of  $\mathcal{K}$  mini-batches (with  $K = |\mathcal{K}|$ , each of size M/K). 501 Then, we use the average advantage within each mini-batch for 502 gradient calculation. The coherence for each mini-batch and 503 the overall batch coherence are calculated using [44, Th. 3] 504 (lines 18 and 19 in Algorithm 2). This theorem ensures 505 statistical equivalence of the per-sample coherence described 506 in (1). 507



# Algorithm 2 Detection Phase for the RL Scheduler

- 1: Input: Batch  $\mathcal{M}$ , mini-batch  $\mathcal{K}$ , Framework activation period P
- 2: Output: Coherence value
- 3: **Initialization:** Learned policy  $\pi_{\theta}$  with parameters  $\theta$ , Trained critic  $V_{\phi}$  with parameters  $\phi$ ,  $\underset{z\sim\mathcal{K}}{\mathbb{E}}[g_z] \coloneqq 0$ ,  $\mathbb{E}_{z \sim \mathcal{K}} [g_z \cdot g_z] \coloneqq 0$ 4: Call robust monitoring framework every *P* seconds
- 5: // Loss calculation
- 6: while Total number of actions < M do
- 7: Get policy actions  $a_t$
- Get value estimates  $V_{\phi}(s_t)$  using trained critic  $V_{\phi}$ 8:
- Get rewards ( $r_t = -$ task execution time) from the 9: system for the completed tasks
- Calculate the advantage function as loss,  $\mathcal{L}_{\theta}$  using 10:  $V_{\phi}(s_t)$  and  $r_t$  for all the actions as given in the equation (2)
- 11: end while
- 12: // Mini-batch gradients and coherence calculation
- 13: for mini-batch from 1 to K do
- Calculate gradients  $(g_z)$  using average  $\mathcal{L}_{\theta}$  from current 14: mini-batch
- 15:
- Update estimate  $\mathbb{E}_{z \sim \mathcal{K}} [g_z] \coloneqq \mathbb{E}_{z \sim \mathcal{K}} [g_z] + g_z$ Update estimate  $\mathbb{E}_{z \sim \mathcal{K}} [g_z \cdot g_z] \coloneqq \mathbb{E}_{z \sim \mathcal{K}} [g_z \cdot g_z] + g_z \cdot g_z$ 16: 17: end for
- 18: Mini-batch Coherence  $\alpha_K = \frac{\sum_{z \sim \mathcal{K}} [g_z] \cdot \sum_{z \sim \mathcal{K}} [g_z]}{\sum_{z \sim \mathcal{K}} [g_z \cdot g_z]}$
- 19: Coherence  $\alpha_M = M \cdot \frac{\alpha_K}{K (K 1)}$

#### 508 D. Application to Other ML-Based DRM Algorithms

The proposed framework uses a loss function and gra-509 510 dients to compute the coherence for detecting workload changes. Hence, it can be applied to monitor the decisions 511 512 of other ML-based schedulers and DRM algorithms that 513 allow runtime gradient calculation. For example, dynamic 514 thermal and power management techniques determine the 515 optimal voltage-frequency pairs for computing cores to meet 516 thermal constraints while preserving performance. These 517 algorithms encompass a variety of approaches, including 518 IL [12], [52], [53] and RL [54], [55] methods. Our framework 519 can work with all these methods to prevent unexpected 520 behavior due to a mismatch between training and runtime <sup>521</sup> inputs. For example, Sartor et al. [12] employed a hierarchical 522 IL framework featuring distinct policies for frequency, core 523 selections, and execution time predictions. Our framework 524 can effectively monitor these policies, utilizing the described 525 policy and expert actions outlined in the study to compute 526 loss and subsequent steps. It can ensure robust performance 527 across various scenarios. In summary, our framework offers 528 monitoring support for any runtime ML-based framework 529 that utilizes gradient-based optimizations, ensuring robustness 530 and reliability across various dynamic runtime management 531 applications.

# E. Response to Significant Workload Changes

The final stage of the proposed runtime monitoring frame- 533 work is the response to significant changes in the workload. 534 The objective of this stage is to detect the substantial changes 535 in the workload to which the trained model does not gener- 536 alize. We compare this detection's coherence  $(\alpha_M)$  against a 537 threshold  $(\tau)$  learned during training. For this purpose, we 538 employ a simple classifier, such as an SVM, to learn the 539 threshold that maximizes the detection accuracy. Coherence 540 values lower than the threshold ( $\alpha_M < \tau$ ) indicate that 541 scheduler decisions are trustworthy and no intervention is 542 required. In contrast, larger coherence values ( $\alpha_M > \tau$ ) require 543 action since they indicate that the model is not generalizing 544 well to coming samples.

There are two possible responses when a significant 546 workload change deems the scheduler unreliable. The most 547 straightforward remedy is to fall back to a traditional algorithm 548 (e.g., the reference scheduler) for actions. The ML scheduler 549 decisions can be monitored during this time until the coherence 550 value moves below the threshold. In this way, the SoC will 551 be protected from unreliable ML decisions. The second option 552 is incrementally training the scheduler to adapt to workload 553 changes, which will conserve the advantages of using ML 554 schedulers. The rest of this section describes how IL and RL 555 schedulers respond when a significant change is triggered. 556

IL Scheduler: The monitoring process for the IL scheduler 557 involves a reference scheduler whose decisions are used to 558 compute the loss function, as shown in Fig. 4. This implies 559 that the reference actions  $(a_t^*)$  for the samples received during 560 monitoring  $(s_t)$  are readily available, making incremental 561 training a practical option. To this end, we utilize these state- 562 action pairs  $(s_t, a_t^*)$  to incrementally train the IL policy. We 563 also measure the overhead of this training process. It takes 564 approximately 2 ms per epoch for incremental training of the 565 IL scheduler on the Nvidia Jetson Xavier NX board [16], a 566 timeframe negligible compared to the domain-specific appli-567 cation lifecycle. The execution of the tasks continues with 568 the previous policy to ensure continuity during this process. 569 Subsequently, the IL scheduler starts using the new policy  $(\hat{\pi})$ , 570 leading to significant benefits detailed in Section V. 571

RL Scheduler: Unlike the IL scheduler case, RL training 572 is unsupervised, learning from rewards (task execution time) 573 provided by the environment (PEs in SoC) rather than a 574 reference. Hence, the corresponding monitoring process does 575 not involve a reference scheduler that gives correct actions. RL 576 scheduler can be trained at runtime using the rewards received 577 at the end of task executions. However, the RL scheduler can 578 make poor decisions during this time, potentially impacting the 579 runtime of tasks it executes. If this degradation in performance 580 is acceptable, the policy can be incrementally updated during 581 the operation. Otherwise, turning it off may be preferable 582 while the coherence value is above the threshold. One can 583 also train an RL policy offline incrementally and update the 584 scheduler if the workload changes are permanent. 585

#### V. EXPERIMENTAL EVALUATION 586

This section presents the experimental evaluations of our 587 framework. We detail the experimental setup in Section V-A. 588

Section V-B discusses the results obtained for the IL scheduler,
while Section V-C presents the findings for the RL scheduler.
Lastly, Section V-D discusses the runtime overhead of our
proposed framework for both schedulers.

#### 593 A. Experimental Setup

Domain-Specific SoC Configuration: The selection of the 594 595 SoC configuration is tailored to the requirements of domain-<sup>596</sup> specific applications. Our simulation configuration consists of sixteen PEs, comprising eight general-purpose cores utilizing 597 598 the Arm big.LITTLE architecture. These cores include four Arm A57 performance and four Arm A53 low-power cores. 599 600 Additionally, the SoC incorporates eight fixed-function accel-601 erators designed for handling intensive tasks: four accelerators 602 dedicated to Fast Fourier Transform, two for Viterbi decod-603 ing, and two for matrix multiplication. This configuration is 604 designed based on the specific demands of the target domain 605 applications and the computational intensities of the tasks in 606 these applications.

*Domain Applications:* The evaluation of the runtime monitoring framework encompasses six real-world applications spanning the telecommunication and RF domains. These applications include WiFi transmitter, WiFi receiver, temporal mitigation, lag detection, single-carrier transmitter, and singlecarrier receiver. The number of tasks for these applications varies from 7 to 34. They are mixed into the workloads spanning from lower to higher intensity levels, ensuring comprehensive coverage, as detailed in [56] and [57].

Simulation Framework: We evaluated our runtime monitoring framework using an open-source discrete event-based against two commercial SoCs, the Odroid-XU3 [58] and the Zynq Ultrascale+ ZCU102 [59]. It enables target application simulations using different schedulers, providing a flexible environment for efficiently implementing new scheduling policies and our framework. Each simulation duration is around applications running, ranging from 4000 to 40 000 instances, and an average task count ranging from 50 000 to 500 000.

#### 627 B. Results Obtained With IL Scheduler

This section delves into the experimental evaluations with IL schedulers. The policy adopted for the IL scheduler comprises a neural network architecture consisting of three dense all layers, each with 32 neurons. The neural network is trained using Python and TensorFlow libraries, achieving accuracies ranging between 96.1% and 98.3% against the reference scheduler, ETF [23]. The policy leverages a combination of system, application, and task-level data as features to determine the cluster assignment. Then, the task is assigned to range set to become available first. We first illustrate the proposed framework as a function of time using single- and multiapplication use cases. Then, we summarize our exhaustive accuracy evaluations.

<sup>642</sup> Single Application Use Case Illustration: This illustrative <sup>643</sup> example starts running a domain application represented in



Fig. 6. Illustration of (a) the coherence value and (b) the average execution time for the runtime monitoring framework with IL scheduler using two applications. The first application (WiFi transmitter) runs until the black dotted line. After that, it is replaced by a new application (lag detection) not represented in the training data.

the training dataset. As the test samples from this application 644 arrive at runtime, the coherence value remains low, as shown 645 in Fig. 6(a). We emphasize that the training and test samples 646 are different except that they come from the same application. 647 Since the execution time varies significantly over time, it 648 cannot be used alone to identify significant workload changes. 649 After running for 0.8 s, this application is replaced with a new 650 one not represented in the training dataset. The proposed mon- 651 itoring framework successfully captures this change, as shown 652 in Fig. 6(a). The coherence increases quickly, indicating the 653 unalignment between the trained policy and the impact of new 654 data samples. If we do not take action (e.g., incrementally train 655 or turn off the scheduler), the coherence remains high, and 656 execution time varies around 200 us. In contrast, incremental 657 training (explained in Section IV-E) successfully adapts the 658 policy to the new application, as revealed by the coherence 659 plot in Fig. 6(a). Furthermore, the execution time reduces 660 on average by 10%. Finally, we note that the incrementally 661 trained policy still runs the first application optimally, i.e., the 662 coherence remains low if it resumes running. This part is not 663 plotted for brevity. 664

Multiple Application Use Case Illustration: The second 665 example starts running a mix of five applications represented 666 in the training dataset. The coherence computed at runtime 667 remains low, as expected, as shown in Fig. 7(a). After running 668 them for about 0.25 s (marked by a dotted line), these applica- 669 tions halt, and a previously unseen application starts running. 670 The proposed framework successfully tracks the increased 671 coherence after this change. As in the previous example, an 672 elevated coherence indicates that new data samples require 673 updating the policy parameters. If the policy is not updated, 674 coherence remains high, and the execution time rises to about 675 2.5 ms, as shown in Fig. 7(b). In contrast, the proposed 676 incremental training rapidly reduces coherence to its original 677 value. Moreover, it achieves a remarkable performance boost 678  $(12 \times \text{ lower execution time})$  compared to no training. 670

Accuracy and Performance Summary: We prepared extensive use case scenarios similar to those illustrated above. 681 They start running a randomly selected subset of application 682



Fig. 7. Illustration of (a) the coherence value and (b) the average execution time for the runtime monitoring framework with IL scheduler using a workload composed of six applications. Five out of six domain applications run concurrently until the black dotted line. After that, the sixth application (single-carrier receiver) is introduced.

TABLE IACCURACY AND EXECUTION TIME IMPROVEMENTS FOR RUNTIMEMONITORING FRAMEWORK ON IL AND RL SCHEDULERS (M = 1024)

Scheduler	Monitoring	False	False	Avg. Exec. Time
	Accuracy	Negative	Positive	Improvement
IL	98.39%	0.59%	1.02%	$4.21 \times$
RL	88.75%	6.20%	5.05%	$1.32 \times$

683 mixes and then randomly change the applications. Single 684 application examples start running one of the six domain 685 applications randomly and switch to another one after a 686 random duration. We repeated these simulations at different 687 intensities and obtained 1221 batches. 663 out of these 1221 <sup>688</sup> points indicate inputs the ML scheduler does not generalize. The multiapplication experiments start running five out of six 689 <sup>690</sup> applications concurrently (leaving one out). Then, the missing application replaces the original one. These experiments are 691 also repeated to obtain 13767 batches. 8585 of these 13767 693 batches correspond to input the ML scheduler does not generalize. Overall, the combined data set comprises 14988 694 batches, of which 9248 batches indicate a significant input 695 696 change.

The proposed runtime monitoring framework identifies 697 whether the IL scheduler generalizes to new data points 698 correctly 98.39% of the time, as summarized in Table I (the 699 700 first row). False positives in Table I occur when our monitoring framework detects activity despite there being no new 701 702 application. False negatives, on the other hand, occur when nongeneralized application appears but is not detected by 703 a 704 our monitoring framework. The IL scheduler's false positive <sup>705</sup> rate is only 1.02%, which means it incorrectly flags a change, 706 although the scheduler generalizes well to the input. More 707 importantly, it almost never misses a significant input change  $_{708}$  (0.59%). Finally, the proposed framework enables  $4.21 \times$ 709 lower execution time on average when incremental training is 710 performed. These results present that the proposed framework 711 can effectively detect when the IL scheduler makes unreli-712 able decisions and adapt the scheduler to achieve substantial 713 benefits.



Fig. 8. Result of (a) the coherence value and (b) the average execution time for the runtime monitoring framework with RL scheduler using a workload comprising instances from two applications. Until the black dotted line, the first (WiFi receiver) application instances are in the system. After that, the new application (temporal mitigation) is introduced.

#### C. Results Obtained With RL Scheduler

This section discusses the performance of the proposed 715 runtime monitoring framework when applied to the RL sched-716 uler. The RL scheduler comprises an actor policy for decision 717 making and a critic network for evaluation. The actor policy 718 is responsible for scheduling decisions and is situated on the 719 critical path of the main process. Therefore, it is implemented 720 using a DDT, enabling scheduling in approximately 0.18  $\mu$ s 721 on the Nvidia Jetson Xavier NX board [16]. Once a scheduling 722 decision is made, the main process executes tasks on PEs while 723 our framework concurrently monitors these decisions in the 724 background. Actor–critic policies utilize features encompassing task, application, and SoC-level information (similar to 726 the IL scheduler described in Section V-B). These policies are 727 trained using PyTorch with an OpenAI Gym environment [60]. 728

We conducted leave-one-out experiments with all six  $^{729}$  domain applications for a comprehensive performance eval- $^{730}$  uation. The RL scheduler generalizes well to five of these  $^{731}$  applications, even when they are excluded from training.  $^{732}$  However, it performs poorly when running the last application  $^{733}$  (temporal mitigation), indicating that the RL scheduler does  $^{734}$  not generalize to this application and does not make robust  $^{735}$  decisions. Our monitoring framework confirms this observation, as coherence values remain consistently low even with the  $^{737}$  arrival of new applications, except for "temporal mitigation,"  $^{738}$  where coherence increases when the RL policy schedules it.  $^{739}$  Each batch (M) used in monitoring comprises 1024 samples,  $^{740}$  each divided into eight mini-batches (K) with 128 samples.  $^{741}$ 

*Single Application Use Case Illustration:* Like the IL <sup>743</sup> experiments in Section V-B, this scenario begins by executing <sup>744</sup> a single application represented in the training dataset. The <sup>745</sup> coherence computed by the proposed framework is low during <sup>746</sup> this time, as illustrated in Fig. 8(a). Subsequently, it is replaced <sup>747</sup> with a new application, to which the RL scheduler does <sup>748</sup> not generalize. The coherence value sharply increases from <sup>749</sup> approximately zero to over 20 following this change, as shown <sup>750</sup> in Fig. 8(a). Correspondingly, there is a sudden decrease in <sup>751</sup>



Fig. 9. Result of (a) the coherence value and (b) the average execution time for the runtime monitoring framework with RL scheduler using a workload comprising instances from six applications. Until the black dotted line, five out of six application instances are present in the system. After that, the sixth application (temporal mitigation) is introduced.

752 execution time, as shown in Fig. 8(b). This decrease occurs because the new application has inherently shorter execution 753 times. However, it is essential to note that a shorter execution 754 time does not necessarily indicate that the RL scheduler has 755 successfully generalized to the new application. As discussed 756 Section IV-E, the policy undergoes incremental offline 757 in 758 training to adjust to a new application. The policy retains its performance for the initial application while being optimized 759 760 for the new one. With the incrementally trained policy, the average execution time decreases by  $1.47 \times$ 761

Multiple Application Use Case Illustration: The multiple 762 763 application begins running five out of six domain applications, 764 like the IL example. Coherence during this time is low since 765 these applications are represented during training, as shown  $_{766}$  in Fig. 9(a). Then, a new application not represented in the 767 training replaces the original mix. The proposed framework successfully captures this change, as indicated by the abrupt 768 increase in coherence after the dotted line. The execution time 769 varies widely during the initial period, but it has a similar 770 average value with lower variation after the new application 771 launched. This behavior shows that execution time is not 772 is reliable indicator of the scheduler's generalizability. Finally, 773 a <sup>774</sup> Fig. 9(b) shows training the scheduler incrementally adapts it the new application, enabling  $1.48 \times$  lower execution time. 775 to Accuracy and Performance Summary: We conclude this 776 section by summarizing the accuracy and performance benefits 777 778 of the proposed framework with RL schedulers. Like the IL 779 experiments, we conducted comprehensive simulations with varying application loads. For single-application examples, we 780 assessed the monitoring framework across a total of 3685 781 <sub>782</sub> batches (each comprising M = 1024 tasks). The RL scheduler 783 fails to generalize to 666 of these batches coming from the 784 new application. In the case of multiple applications, we evaluated our monitoring framework for over 1168 batches, with 161 batches indicating a lack of generalization. Overall, 786 we evaluated our monitoring framework for 4853 batches. As 788 discussed previously, the RL scheduler demonstrates inherent 789 generalization to five applications, resulting in fewer instances 790 of nongeneralized cases than the IL scheduler.

TABLE II MONITORING FRAMEWORK OVERHEAD FOR IL SCHEDULER ON NVIDIA JETSON XAVIER NX BOARD [16]

Batch size (M)	Reference scheduler (ms)	Loss (ms)	Gradient (ms)	Coherence (ms)	Total overhead (ms)
128	3.20	1.25	7.84	0.09	12.38
256	6.40	1.92	13.70	0.21	22.23
512	12.80	3.69	26.48	0.29	43.26
1024	25.60	7.50	50.24	0.40	83.74

Table I summarizes the proposed monitoring framework's 791 accuracy and performance benefits. It can determine whether 792 the scheduler generalizes to the new inputs or not with 88.75% 793 accuracy. Closing inspection reveals a 6.2% false negative 794 rate, i.e., the frequency of failing to detect a new application. 795 Similarly, it incorrectly flags the lack of generalization to a 796 new application (false positive) for 5.05% of the batches. The 797 values are lower than those obtained with the IL scheduler 798 since there is no ground truth label during the training and 799 monitoring of the RL scheduler. Hence, it only relies on the 800 reward signal, a weaker indication of correctness than the 801 ground truth. The accuracy can be improved by checking more 802 than one batch before flagging a lack of generalization at the 803 expense of more significant overhead. This optimization is one 804 of the potential future research directions. Finally, when the 805 proposed framework identifies a new application, incremental 806 training provides, on average, a  $1.32 \times$  lower execution time. 807

Application to Other Schedulers: The proposed framework 808 can also be used with other scheduling algorithms besides 809 the IL and RL schedulers considered so far. For example, 810 Decima [7] is a graph neural network-based job scheduling 811 algorithm targeting data clusters for streaming applications. 812 The authors show that when the model is trained with low 813 throughput workloads, the model poorly generalizes to high 814 throughput workloads, leading to a 1.6× higher average 815 execution time. This example shows a great use case for our 816 monitoring framework. As it successfully detects the changes 817 in the applications, specifically an unseen level of throughput 818 in this case, it can trigger the system to take proper action, such 819 as incremental training. Indeed, when the incrementally trained 820 version performs similarly to a system trained with both high 821 and low throughput workloads, the proposed framework can 822 achieve 1.37× faster execution time. Therefore, our framework 823 is effective for a wide range of hardware platforms and models 824 that utilize gradient descent optimization. 825

### D. Overhead Analysis

The proposed monitoring framework is not on the critical <sup>827</sup> path since it operates in the background. An overhead analysis <sup>828</sup> is still helpful since it helps determine how frequently the <sup>829</sup> monitoring can be triggered. As described in Section III-B, <sup>830</sup> this work considers domain-specific SoC, where applications <sup>831</sup> continuously process streaming inputs for extended durations <sup>832</sup> after launch. The proposed monitoring framework does not <sup>833</sup> need to run continuously. It can be triggered 1) when a new <sup>834</sup> application launches or 2) periodically while sleeping most of <sup>835</sup> the time. The overhead analysis in this section summarizes the <sup>836</sup> execution overhead as a function of the batch size (M). These <sup>837</sup> values determine the shortest possible monitoring period.

TABLE III MONITORING FRAMEWORK OVERHEAD FOR RL SCHEDULER ON NVIDIA JETSON XAVIER NX BOARD [16]

Batch size (M)	Value estimates (ms)	Loss (ms)	Gradient (ms)	Coherence (ms)	Total overhead (ms)
128	0.02	12.22	20.09	0.10	32.42
256	0.04	24.11	20.20	0.10	44.45
512	0.08	49.01	23.83	0.10	73.03
1024	0.17	92.30	24.96	0.10	117.53

Table II summarizes the overhead for monitoring the IL 839 840 scheduler when running on the Nvidia Jetson Xavier NX <sup>841</sup> board [16]. The most time-consuming step is the gradient s42 calculation, varying from 7.84 to 50.24 ms as the batch size 843 grows from 128 to 1024. The second largest contributor is <sup>844</sup> running the reference scheduler, which takes 3.2–25.6 ms. We emphasize that different components of the monitoring 845 846 framework can be pipelined. For example, the reference 847 scheduler can start running for the next task after the loss <sup>848</sup> calculation begins. Hence, the total execution time in Table II a loose upper bound. Regardless, our measurements show 849 is 850 that the entire monitoring process takes 83.74 ms, even for the largest batch size used in our experiments, highlighted 851 852 in the table. A smaller batch size can be employed to speed up the monitoring process at the expense of accuracy. The 853 <sup>854</sup> last row (bold) highlights the setting in our experiments, while 855 evaluations shown for all batch sizes are listed in Table II. This means the monitoring can be repeated in the background 856 with this period if needed. However, in practice, we expect 857 858 a more extended period, on the order of seconds, since the <sup>859</sup> application composition in the target SoCs rarely changes.

Table III summarizes the monitoring and detection overhead 860 for RL schedulers as a function of the batch size (M). The loss 862 and gradient calculations dominate the total execution time 863 for RL schedulers. The loss takes longer than those for the <sup>864</sup> IL scheduler since loss for IL is the mean squared error, but <sup>865</sup> RL requires solving (2). As in the IL scheduler, the value 866 estimate, loss, and gradient calculations can be pipelined. In <sup>867</sup> the worst case, when all steps are performed sequentially, the total execution time varies from 32.42 to 117.53 ms as 868 <sup>869</sup> the batch size grows from 128 to 1024. The last row (bold) <sup>870</sup> highlights the setting in our experiments. Like in the IL case, <sup>871</sup> all batch sizes in the table lead to effective monitoring. Hence, 872 the proposed framework can run as a real-time background 873 task to monitor RL schedulers.

As detailed in Section IV-B, coherence can be calculated <sup>876</sup> using a running sum for  $\mathbb{E}_{z\sim\mathcal{M}}[g_z]$  and  $\mathbb{E}_{z\sim\mathcal{M}}[g_z \cdot g_z]$  vectors. <sup>876</sup> This approach ensures that the memory requirement does not <sup>877</sup> scale with the batch size M, meaning the memory requirement <sup>878</sup> remains constant, or O(1). Using onboard sensors, we also <sup>879</sup> monitored power utilization and temperature changes on the <sup>880</sup> Jetson Xavier NX. We observed that it consistently consumes <sup>881</sup> less than 1 W of power. So, our monitoring framework requires <sup>882</sup> a maximum of 83.74 mJ for the IL case and 117.53 mJ for <sup>883</sup> the RL case. Due to this very low energy consumption, we <sup>884</sup> observed only a 3 °C-4 °C increase in temperature. This <sup>885</sup> analysis shows that our monitoring framework has a negligible <sup>886</sup> impact compared to the application running on the target <sup>887</sup> SoCs.

#### VI. CONCLUSION

ML algorithms are increasingly used for runtime decision <sup>889</sup> making in SoC. For example, offline-trained deep neural <sup>890</sup> networks and DDT policies schedule tasks to PEs. Like all ML <sup>891</sup> models that critically depend on training data, these schedulers <sup>892</sup> can exhibit unpredictable behavior when the runtime inputs <sup>893</sup> deviate significantly from the training. Hence, monitoring their robustness and protecting the system from adverse effects is <sup>895</sup> crucial. <sup>896</sup>

This article introduces a novel runtime monitoring framework for domain-specific SoCs. The proposed framework uses the new input samples and policy gradient to compute a coherence metric. Low coherence indicates agreement with the trained policy and new inputs, while elevated coherence shows that the scheduling decisions are unreliable. We also discuss how the policies can be incrementally trained or turned off until they become reliable. Extensive evaluations show that the proposed framework can detect when the scheduler decisions are unreliable with 88.75%–98.39% accuracy. Our experiments also reveal that  $1.1 \times -14 \times$  lower execution time is possible by incremental retraining.

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