

# Time-Triggered Scheduling for Nonpreemptive Real-Time DAG Tasks Using 1-Opt Local Search

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**Abstract**—Modern real-time systems often involve numerous computational tasks characterized by intricate dependency relationships. Within these systems, data propagate through cause-effect chains from one task to another, making it imperative to minimize end-to-end latency to ensure system safety and reliability. In this article, we introduce innovative nonpreemptive scheduling techniques designed to reduce the worst-case end-to-end latency and/or time disparity for task sets modeled with directed acyclic graphs (DAGs). This is challenging because of the noncontinuous and nonconvex characteristics of the objective functions, hindering the direct application of standard optimization frameworks. Customized optimization frameworks aiming at achieving optimal solutions may suffer from scalability issues, while general heuristic algorithms often lack theoretical performance guarantees. To address this challenge, we incorporate the “1-opt” concept from the optimization literature (Essentially, 1-opt means that the quality of a solution cannot be improved if only one single variable can be changed) into the design of our algorithm. We propose a novel optimization algorithm that effectively balances the tradeoff between theoretical guarantees and algorithm scalability. By demonstrating its theoretical performance guarantees, we establish that the algorithm produces 1-opt solutions while maintaining polynomial run-time complexity. Through extensive large-scale experiments, we demonstrate that our algorithm can effectively reduce the latency metrics by 20% to 40%, compared to state-of-the-art methods.

**Index Terms**—End-to-end latency, optimization, real-time system, scheduling, time-triggered scheduling (TTS).

## I. INTRODUCTION

ENSURING timeliness, short end-to-end latency, and small data communication time disparity is a paramount consideration across various domains, including control engineering, body electronics, and automotive systems [1]. For example, the

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RTSS2021 Industry Challenge [2] underscores the importance of bounding worst-case end-to-end latency and time disparity in *nonpreemptive* autonomous driving systems. Nonpreemptive systems are becoming more popular due to the wide adoption of single-instruction-multidata (SIMD) computing architectures such as GPU. Since preemption with GPU usually has a much higher overhead than CPU devices, embedded GPU devices often only provide limited, if any, support for preemption [3].

Scheduling and optimizing systems with respect to data age, reaction time, and time disparity (DARTD)<sup>1</sup> pose significant challenges [1], [4], [5], [6], [7], [8] due to their nonconvex and noncontinuous characteristics. These attributes hinder the application of standard mathematical programming frameworks, such as integer linear programming (ILP) and convex optimization. However, naively employing highly general optimization frameworks like meta-heuristics often lacks theoretical performance guarantees. Conversely, developing customized frameworks targeted at yielding optimal solutions [7] encounters scalability issues, which is particularly important in modern computation systems, where hundreds of computation tasks may exist [9], [10]. To tackle these challenges, we propose a computationally efficient optimization algorithm with some theoretical performance guarantees.

In this article, we leverage the *1-opt* concept, drawn from the optimization literature [11], [12] as a foundation in the development of our optimization algorithm. A solution vector  $\mathbf{x} \in \mathbb{R}^N$  for an optimization problem is called 1-opt if changing any single component  $x_i \in \mathbf{x}$  does not result in an improvement beyond the current solution  $\mathbf{x}$ . We refer to algorithms that yield 1-opt solutions as 1-opt algorithms. In contrast to heuristic algorithms, 1-opt algorithms provide stronger theoretical performance guarantees. Moreover, they often demonstrate superior scalability when compared to algorithms aimed at finding optimal solutions.

Nevertheless, constructing 1-opt algorithms for optimizing nonconvex and noncontinuous metrics, such as DARTD, is very challenging. Naively employing brute-force algorithms can result in exponential complexity in worst-case scenarios. To address this, we propose a novel algorithm that employs

<sup>1</sup>Given a cause-effect chain, data age measures the maximum duration for which a sensor event influences the computational system, while reaction time measures the maximum latency for the system to first react to a sensor event. Additionally, time disparity quantifies the maximum difference in the generation times of multiple source data from which one task reads input.

a technique to partition the solution space into multiple convex subspaces, allowing for the efficient utilization of linear programming (LP) to minimize DARTD within each subspace. Subsequently, an iterative subroutine efficiently traverses among the subspaces, ensuring that the output is 1-opt. Furthermore, we prove that the solution of each LP is local optimal in nonpreemptive single-core systems. In comparison with simple scheduling heuristics, such as list scheduling [13], scheduling with LP can explore a much larger solution space, leading to enhanced performance. Moreover, the polynomial run-time complexity of solving LP enhances algorithm scalability compared to optimal algorithms that exhibit exponential run-time complexities in the worst case. Finally, to further improve the efficiency of LP, we propose an algorithm capable of efficiently performing nonpreemptive schedulability analysis.

*Contributions:* Our contributions in this article are as follows.

- 1) We employ the 1-opt concept in the development of schedule optimization algorithms. To the best of our knowledge, this is the first work to utilize the 1-opt concept in real-time system scheduling problems, and it achieves superior performance compared to state-of-the-art methods.
- 2) We propose a novel optimization framework designed to minimize worst-case DARTD, which is proven to yield 1-opt solutions with only polynomial run-time complexity.
- 3) To the best of our knowledge, this is the first work that considers optimizing time disparity with time-triggered scheduling (TTS).
- 4) Large-scale experiments demonstrate that 1-opt methods achieve 20% to 40% latency reductions and enhanced scalability compared to state-of-the-art techniques.

## II. RELATED WORK

As an important indicator of system safety, end-to-end latency has been thoroughly studied. Numerous analyses have delved into cause-effect chains or task sets structured with directed acyclic graphs (DAGs) dependency [1], [4], [5], [7], [8], [14], [15]. These analytical approaches address diverse scenarios, including different scheduling algorithms (e.g., fixed-priority scheduling and earliest deadline first scheduling) and communication protocols (e.g., implicit communication and logical execution time (LET)). Moreover, some studies explore temporal variations across various contexts [16], [17]. Beyond the analysis of end-to-end latency, a considerable body of work focuses on scheduling and the schedulability of DAG task sets [18], [19], [20], [21]. These comprehensive analyses build the foundation for the optimization works performed in this article.

General optimization techniques in real-time systems can be broadly categorized into two categories: 1) heuristic algorithms with general applicability but lacking solution quality guarantees [10], [22] and 2) optimal algorithms built with sophisticated assumptions and problem modeling [7], [23], [24]. However, the latter may encounter scalability issues when facing large-scale optimization

problems and the performance may also degrade seriously. Considering the challenge of finding the “perfect” algorithms (optimal and fast) for many real-world problems, algorithm designers often face a tradeoff between solution quality and run-time complexity.

There are many works that optimize the end-to-end latency with different types of variables. Within the LET protocol, many works consider optimizing the time to read/write data, where both optimal [25], [26] and heuristic [27], [28] algorithms have been proposed. Some other works consider implicit communication protocol, primarily concentrating on optimizing task schedules [7]. Besides, there are also works that improve different metrics related to end-to-end latency by performing priority assignments [29], [30].

This article differs from existing literature in proposing to use a new concept, 1-opt, to guide the algorithm design process. We also designed a novel optimization algorithm which is proved to find 1-opt solutions and demonstrated to achieve significantly better performance than the state-of-the-art methods.

## III. SYSTEM MODEL AND PROBLEM DESCRIPTION

In this article, bold fonts are used to represent vectors or sets, while light characters denote scalars or individual elements. The double bars notation  $|||$  denotes norm-2. During iterations, the  $k$ th iteration is denoted by a superscript, such as  $\mathbf{x}^{(k)}$ .

### A. System Model

We consider a multirate DAG model  $\mathcal{G} = (\boldsymbol{\tau}, \mathbf{E})$ , in which each task  $\tau_i \in \boldsymbol{\tau}$  is represented as a node, and a directed edge  $E_k \in \mathbf{E}$  from  $\tau_i$  to  $\tau_j$  denotes that  $\tau_j$  reads input from  $\tau_i$ . The total number of tasks in  $\boldsymbol{\tau}$  is denoted as  $n$ . Each task releases jobs (i.e., *instances of the task*) periodically with a nominal period. A task  $\tau_i$  is characterized by a tuple  $\{T_i, C_i, D_i\}$ , which denotes the period, worst-case execution time (WCET), and the relative deadline, respectively. We assume  $D_i \leq T_i$ . The  $k$ th released job of  $\tau_i$  is denoted as  $J_{i,k}$  and it is released at the time  $k \cdot T_i$ . The DAG  $\mathcal{G}$  is not necessarily fully connected. Without loss of generality, we assume all the tasks are released simultaneously at time 0. However, if there is an offset when all the tasks are initially released, our optimization algorithm can also be applied by modifying the schedulability analysis algorithms and optimization constraints accordingly.

The hyper-period (i.e., the least common multiple of periods of all tasks in  $\mathcal{G}$ ) is denoted as  $H$ . Within a hyper-period, each job  $J_{i,k}$  starts execution at time  $s_{i,k}$  *nonpreemptively* and finishes at  $f_{i,k} = s_{i,k} + C_i$ . Such a nonpreemptive policy eliminates preemption overhead, which could be large in GPU computation. The total number of jobs within a hyper-period is denoted as  $N$ . Potential generalizations into preemptive systems are discussed in Section VIII-B.

In a DAG  $\mathcal{G}$ , tasks with chained reading/writing dependency formulate a cause-effect chain  $\mathcal{C} = \{\tau_{p_0} \rightarrow \tau_{p_1} \rightarrow \dots \rightarrow \tau_{p_k}\}$ , which represents a data communication path. The implicit communication protocol [31] is utilized in data communication where each job  $J_{i,k}$  reads data at its start time  $s_{i,k}$ , and writes data at  $f_{i,k} = s_{i,k} + C_i$  even if  $J_{i,k}$  may finish

earlier than its WCET. Multiple cause–effect chains may share tasks, and the set of cause–effect chains is denoted as  $\mathcal{C}$ .

In scenarios where a single task reads data from the outputs of multiple tasks, we refer to the tasks providing data as the source tasks, and the task that reads these outputs as the sink task. The source tasks and the sink task collectively formulate a “merge”  $\mathcal{M}$  (For example, see Example 1). The set containing all merges to be optimized is denoted as  $\mathcal{M}$ .

The DAG task set is processed by a multiprocessor system. We assume that each job has a known processor assignment before performing the schedule optimization, and we do not consider processor migration during execution. For presentation simplicity, we assume using a homogeneous multiprocessor system. However, the heterogeneous computation can be handled easily by modifying the resource-bound constraint correspondingly after obtaining processor assignments. In experiments, the processor is assigned following the First-Come-First-Serve heuristic, same as Verucchi et al. [7] for a fair comparison. The proposed optimization framework does not optimize processor assignments.

### B. General Schedule Optimization Problem Formulation

We consider the schedule optimization problem of time-triggered systems, focusing on reducing the worst-case end-to-end latency and/or time disparity. The optimization variables for our scheduling problem are called a schedule.

**Definition 1 (Schedule):** Given a DAG  $\mathcal{G} = (\tau, \mathbf{E})$ , a schedule  $s \in \mathbb{R}^N$  is a vector of the start time of all jobs of all tasks in  $\tau$  within a hyper-period  $H$ .

A general schedule optimization problem consists of an objective function and a set of schedulability constraints

$$\text{Minimize}_{s} \mathcal{F}(s) \quad (1)$$

Subject to:

$$\forall i \in \{0, \dots, n-1\} \quad \forall k \in \{0, \dots, H/T_i - 1\}$$

$$k \cdot T_i \leq s_{i,k} \leq k \cdot T_i + D_i - C_i \quad (1a)$$

$$\text{ResourceBound}(s) = 0. \quad (1b)$$

Constraint (1a) guarantees every job starts and finishes within its schedulable range. The resource bound (1b) specifies that no computation resources are overloaded (e.g., one CPU core executes more than one job simultaneously). The specific form of (1b) will be introduced later in Section III-E. A schedule  $s$  is feasible (or equivalently, schedulable) if it satisfies (1a) and (1b). Given a schedule  $s$ , the finish time  $f_{i,k}$  of each job  $J_{i,k}$  in nonpreemptive systems is implicitly decided:  $f_{i,k} = s_{i,k} + C_i$ .

### C. Example Problem—End-to-End Latency Optimization

Each cause–effect chain  $\mathcal{C}$  could trigger multiple job chains within a hyper-period. The worst-case data age (reaction time) of a cause–effect chain  $\mathcal{C}$  is the length of its longest immediate backward (forward) job chain [5], [6]. These definitions are briefly reviewed below:

**Definition 2 (Job Chain [5], [6]):** Given a cause–effect chain  $\mathcal{C} = \{\tau_{p_0} \rightarrow \tau_{p_1} \rightarrow \dots \rightarrow \tau_{p_k}\}$ , a job chain  $\mathcal{C}^J$  is a sequence of jobs  $\{J_{p_0,q_0} \rightarrow J_{p_1,q_1} \rightarrow \dots \rightarrow J_{p_k,q_k}\}$ , where

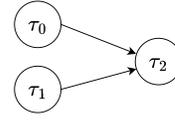


Fig. 1. Example DAG.

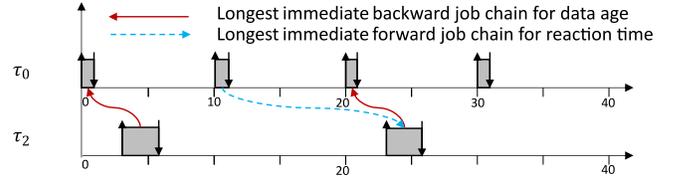


Fig. 2. Longest immediate forward and backward job chains for cause–effect chain  $\mathcal{C} = \{\tau_0 \rightarrow \tau_2\}$ .

$J_{p_i,q_i}$  is the  $q_i^{\text{th}}$  job of  $\tau_{p_i}$ , and the data produced by  $J_{p_i,q_i}$  is read by  $J_{p_{i+1},q_{i+1}}$ .

**Definition 3 (Length of a Job Chain):** The length of a job chain  $\mathcal{C}^J = \{J_{p_0,q_0} \rightarrow J_{p_1,q_1} \rightarrow \dots \rightarrow J_{p_k,q_k}\}$  is the time interval from the start time of  $J_{p_0,q_0}$  till the finish time of  $J_{p_k,q_k}$ . It is denoted as  $L(\mathcal{C}^J) = f_{p_k,q_k} - s_{p_0,q_0}$ .

**Definition 4 (Immediate Backward (Forward) Job Chain [5], [6]):** A job chain  $\mathcal{C}^J = \{J_{p_0,q_0} \rightarrow J_{p_1,q_1} \rightarrow \dots \rightarrow J_{p_k,q_k}\}$  is the immediate backward (forward) chain under schedule  $s$  if (2) (3) is satisfied

$$\forall i \in \{1, \dots, k\}, \quad f_{p_{i-1},q_{i-1}} \leq s_{p_i,q_i} < f_{p_{i-1},(q_{i-1}+1)} \quad (2)$$

$$\forall i \in \{0, \dots, k-1\}, \quad s_{p_{i+1},(q_{i+1}-1)} < f_{p_i,q_i} \leq s_{p_{i+1},q_{i+1}}. \quad (3)$$

**Example 1:** Fig. 1 shows a simple DAG with three tasks:  $\tau = \{\tau_0, \tau_1, \tau_2\}$  and two edges:  $\mathbf{E} = \{\tau_0 \rightarrow \tau_2, \tau_1 \rightarrow \tau_2\}$ . The WCET, period, and relative deadline of each task is:  $\{C_0 = 1, T_0 = 10, D_0 = 10\}$ ,  $\{C_1 = 2, T_1 = 20, D_1 = 20\}$ ,  $\{C_2 = 3, T_2 = 20, D_2 = 20\}$ . The task set is executed on 2 identical processors unless otherwise stated. The hyper-period is 20. The schedule variable contains the start time of  $N = 4$  jobs:  $s = [s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}]$ .

Suppose we have a schedule  $s = [0, 10, 1, 3]$ . For the cause–effect chain  $\mathcal{C} = \{\tau_0 \rightarrow \tau_2\}$ , the job chain  $\mathcal{C}_0^J = \{J_{0,0} \rightarrow J_{2,0}\}$  is both an immediate backward job chain and immediate forward job chain with length  $L(\mathcal{C}_0^J) = 6$ .  $\mathcal{C}_1^J = \{J_{0,1} \rightarrow J_{2,1}\}$  is another immediate forward job chain with length  $L(\mathcal{C}_1^J) = 16$ . Thus,  $\max \text{DA}_{\mathcal{C}}(s) = 6$ ,  $\max \text{RT}_{\mathcal{C}}(s) = 16$ . The longest job chains for this scenario are shown in Fig. 2.

Given a schedule  $s$ , we use  $\text{DA}_{\mathcal{C}}(s)$  ( $\text{RT}_{\mathcal{C}}(s)$ ) to denote the vector of data age (reaction time) for all job chains of a cause–effect chain  $\mathcal{C}$  within a hyper-period.

To summarize, when optimizing the worst-case data age or reaction time, the objective function in (1) becomes

$$\mathcal{F}(s) = \sum_{\mathcal{C} \in \mathcal{C}} \max \text{DA}_{\mathcal{C}}(s) \quad (4)$$

or

$$\mathcal{F}(s) = \sum_{\mathcal{C} \in \mathcal{C}} \max \text{RT}_{\mathcal{C}}(s). \quad (5)$$

### 274 D. Example Problem—Time Disparity Optimization

275 Similar to a cause–effect chain, a merge  $\mathcal{M}$  may have  
276 multiple job-level merges.

277 *Definition 5 (Job Merge):* A job merge  $\mathcal{M}^J$  contains a sink  
278 job  $J_{j,l}$  and a set of source jobs  $\mathbf{J}_{j,l}^{Src}$ , from which  $J_{j,l}$  directly  
279 reads data

$$280 \quad \forall J_{i,k} \in \mathbf{J}_{j,l}^{Src}, \quad f_{i,k} \leq s_{j,l} < f_{i,k+1}. \quad (6)$$

281 *Definition 6 (Time Disparity [2], [26]):* The time disparity  
282 of a job merge  $\mathcal{M}^J$ , denoted as  $TD(\mathcal{M}^J)$ , is defined as the  
283 difference between the earliest and latest finish times of all  
284 source jobs in  $\mathcal{M}^J$

$$285 \quad TD(\mathcal{M}^J) = \max_{J \in \mathbf{J}_{j,l}^{Src}} f_J - \min_{J \in \mathbf{J}_{j,l}^{Src}} f_J \quad (7)$$

286 where  $f_J$  represents the finish time of a job  $J$ .

287 Given a schedule  $s$ , we use  $\mathbf{TD}_{\mathcal{M}}(s)$  to denote the vector  
288 of time disparities for all job merges of  $\mathcal{M}$  within a hyper-  
289 period. When optimizing the worst-case time disparity metric,  
290 the objective function in (1) is formulated as follows:

$$291 \quad \mathcal{F}(s) = \sum_{\mathcal{M} \in \mathcal{M}} \max \mathbf{TD}_{\mathcal{M}}(s). \quad (8)$$

292 Other forms of the objective functions are discussed in  
293 Section VIII-A.

294 *Example 2:* In Example 1, there is only one merge  $\mathcal{M}$  in  
295 the DAG with  $\tau_2$  as the sink task. The corresponding job merge  
296 has  $J_{2,0}$  as the sink job and  $\{J_{0,0}, J_{1,0}\}$  as the source jobs. The  
297 maximum time disparity is  $\max \mathbf{TD}_{\mathcal{M}}(s) = 3 - 1 = 2$ .

298 *Theorem 1:* The objective functions (4), (5), and (8) are all  
299 nonconvex.

300 *Proof:* We prove it by providing counter-examples.  
301 Remember that a function  $f: \mathbb{R}^n \rightarrow \mathbb{R}$  is convex if for all  $s^{(1)}$   
302 and  $s^{(2)}$  in its domain and  $\forall t \in [0, 1]$ , we have  $f(ts^{(1)} + (1-t)s^{(2)}) \leq tf(s^{(1)}) + (1-t)f(s^{(2)})$ . We now give a counterexample  
303 for reaction time, and the counterexamples for data age and  
304 time disparity are similar. In Example 1, consider  $s^{(1)} =$   
305  $[0, 10, 1, 3]$  with a reaction time 16,  $s^{(2)} = [0, 10, 1, 11]$  whose  
306 reaction time is 14. If we define  $t = 0.5$ , then  $s^{(t)} = ts^{(1)} +$   
307  $(1-t)s^{(2)} = [0, 10, 1, 7]$ , but the reaction time of  $s^{(t)}$  is 20,  
308 which violates the property required by convex functions. ■  
309

### 310 E. Resource Bound Constraint—Interval Overlapping Test

311 In a nonpreemptive system, the interval overlapping Test  
312 (IO Test) analyzes whether processors are overloaded (one  
313 processor executes multiple jobs in parallel) for a given  
314 schedule  $s$ . In this case, each job  $J_{i,k}$  can be modeled as an  
315 interval  $[s_{i,k}, f_{i,k}]$  that starts execution at  $s_{i,k}$  and finishes at  
316  $f_{i,k} = s_{i,k} + C_i$ . Inspired by the demand bound function [32],  
317 we propose an efficient nonpreemptive schedulability analysis  
318 for optimization. Intuitively speaking, there are no overloaded  
319 processors if any two job intervals mapped to the same  
320 processor do not overlap.

321 *Theorem 2 (IO Test):* In nonpreemptive systems, there are  
322 no overloaded processors if the following inequality holds for  
323 any two jobs  $J_{i,k}$  and  $J_{j,l}$  assigned to the same processor:

$$324 \quad \text{if } f_{j,l} \geq s_{i,k}, \text{ then } f_{j,l} - s_{i,k} \geq C_i + C_j. \quad (9)$$

*Proof:* Prove by contradiction. If there are overloaded  
325 processors, by definition, there must be two job execution  
326 intervals overlapping with each other. Let us denote the job  
327 with a larger finish time as  $J_{j,l}$ , the other job as  $J_{i,k}$ , then we  
328 have  
329

$$f_{j,l} - s_{i,k} < C_i + C_j. \quad (10) \quad 330$$

This contradicts the IO test assumption above. ■ 331

*Theorem 3:* Given a set of job intervals  $\mathbf{I} = \{[s_{i,k}, f_{j,l}]\}$   
332 sorted based on its start time  $s_{i,k}$  in increasing order, no  
333 intervals overlap with each other if any two adjacent job  
334 intervals do not overlap with each other.  
335

*Proof:* Skipped. It can be proved easily by contradiction. ■ 336

337 Since a schedule will repeat in every hyper-period, the IO  
338 test only needs to consider all jobs within a hyper-period.  
339 Within partitioned scheduling, each processor has to be tested  
340 separately. The time complexity of the IO test is  $O(N \log(N))$ .

341 *Example 3:* Let us continue with the task set in Example 1.  
342 Suppose we only have one processor and have a schedule  $s =$   
343  $[0, 10, 1, 3]$ . If without sorting, the IO test requires verifying  
344 whether the following six pairs of intervals overlap:

$$345 \quad \begin{aligned} & \{[s_{0,0}, f_{0,0}], [s_{0,1}, f_{0,1}]\} \quad \{[s_{0,0}, f_{0,0}], [s_{1,0}, f_{1,0}]\} \\ & \{[s_{0,0}, f_{0,0}], [s_{2,0}, f_{2,0}]\} \quad \{[s_{0,1}, f_{0,1}], [s_{1,0}, f_{1,0}]\} \\ & \{[s_{0,1}, f_{0,1}], [s_{2,0}, f_{2,0}]\} \quad \{[s_{1,0}, f_{1,0}], [s_{2,0}, f_{2,0}]\}. \end{aligned} \quad 346 \quad 347$$

348 With sorting, only the following three pairs require verifica-  
349 tion:

$$350 \quad \begin{aligned} & \{[s_{0,0}, f_{0,0}], [s_{1,0}, f_{1,0}]\} \quad \{[s_{1,0}, f_{1,0}], [s_{2,0}, f_{2,0}]\} \\ & \{[s_{2,0}, f_{2,0}], [s_{0,1}, f_{0,1}]\}. \end{aligned} \quad 351$$

352 If there is no overlap, then the IO test states that the processor  
353 is not overloaded.

354 Now, we can give the complete form of the resource  
355 bound (1b) in nonpreemptive systems

$$\text{ResourceBound}(s) = \begin{cases} 0, & \text{if } s \text{ passes IO test} \\ 1, & \text{otherwise.} \end{cases} \quad (11) \quad 356$$

### 357 F. Model Assumptions

358 *Assumption 1:* The start time of each job could take con-  
359 tinuous value.

360 Although the computer time is integer multiples of CPU  
361 cycles, the very high-CPU run-time frequency (MHz or GHz)  
362 means that rounding a float-point number into its adjacent  
363 integers only incurs a small precision loss in timing metrics, if  
364 the jobs' relative reading/writing time order remains the same.

365 *Assumption 2:* A feasible schedule [a solution that satis-  
366 fies (1a) and (1b)] is available to start the iterative algorithms  
367 introduced next.

368 Normally, Assumption 2 can be easily satisfied with simple  
369 list schedulers [7]. This article focuses on optimizing the  
370 timing metrics rather than finding a schedulable schedule,  
371 although such an extension is possible (see Section VIII-C).

### 372 G. Challenges

373 Solving the optimization problem (1) for DARTD is difficult  
374 because the objective function follows a *nonlinear, nonmono-*  
375 *tonic, nonconvex, and noncontinuous* relationship with the

variables (see Theorem 1 and its proof). Therefore, most popular optimization frameworks cannot be directly utilized except ILP. However, ILP requires introducing many extra binary variables and could suffer from bad algorithm scalability.

#### IV. JOB ORDER AND SCHEDULING

The proposed optimization framework that solves the problem (1) is built upon the concept of the job order, which specifies the jobs' reading/writing relationships and simplifies the problem into a set of LP problems.

##### A. Job Order

*Definition 7 (Job Scheduling Time):* The job scheduling time of a job  $J_{i,k}$  is denoted as  $\mathcal{T}_{i,k}$ , which could be either the start time (denoted as  $\mathcal{T}_{i,k}^s$ , called *scheduling start time*) or the finish time (denoted as  $\mathcal{T}_{i,k}^f$ , called *scheduling finish time*) of  $J_{i,k}$ .

Since we adopt the implicit communication protocol and nonpreemptive scheduling, a job  $J_{i,k}$ 's reading time is its start time, and its writing time is its finish time.

*Example 4:* In Example 1, consider a schedule  $s = [0, 10, 1, 3]$ . The job  $J_{0,0}$  has two scheduling times: 1) scheduling start time  $\mathcal{T}_{0,0}^s = 0$  and 2) scheduling finish time  $\mathcal{T}_{0,0}^f = 1$ .

*Definition 8 (Job Order):* Given a set of jobs  $\mathbf{J}$ , a job order  $\mathcal{O}$  of  $\mathbf{J}$  is an ordered list containing all job scheduling times (both start and finish) of all the jobs in  $\mathbf{J}$ . The job scheduling times are ordered in nondecreasing order.

For notation convenience, we use  $\mathcal{O}(i)$  to denote the  $i$ th job scheduling time in the job order  $\mathcal{O}$ . For any two job scheduling times  $\mathcal{T}_{i,k}, \mathcal{T}_{j,l} \in \mathcal{O}$ , if  $\mathcal{T}_{i,k}$  has a smaller index than  $\mathcal{T}_{j,l}$  in  $\mathcal{O}$ , denoted as  $\mathcal{T}_{i,k} < \mathcal{T}_{j,l}$ , then that means  $\mathcal{T}_{i,k}$  happens earlier than or at the same time as  $\mathcal{T}_{j,l}$ .

*Example 5:* Consider the task set in Example 1. There are four jobs within a hyper-period. For a schedule  $s = [s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}] = [0, 10, 1, 3]$ , its job order is  $\mathcal{O} = \{\mathcal{T}_{0,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{1,0}^s, \mathcal{T}_{1,0}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f\}$ . We also give two examples for indexing: 1)  $\mathcal{O}(0) = \mathcal{T}_{0,0}^s$  and 2)  $\mathcal{O}(3) = \mathcal{T}_{1,0}^f$ .

A job order  $\mathcal{O}$  implies a set of linear constraints on the schedule  $s$  of the optimization problem (1)

$$\forall i < j, \text{Time}(\mathcal{O}(i)) \leq \text{Time}(\mathcal{O}(j)) \quad (12)$$

where  $\text{Time}(\mathcal{T}_{i,k})$  denotes the time that  $\mathcal{T}_{i,k}$  happens. If  $\mathcal{T}_{i,k}$  is a scheduling start time,  $\text{Time}(\mathcal{T}_{i,k}) = s_{i,k}$ , otherwise,  $\text{Time}(\mathcal{T}_{i,k}) = s_{i,k} + C_i$ .

##### B. Scheduling With Job Order

Finding a schedule that satisfies a given job order  $\mathcal{O}$  is equivalent to solving the problem (1) with extra linear constraints given by (12). Here, we provide the job order scheduling problem for  $\mathcal{O}$

$$\text{Minimize}_s \mathcal{F}(s) \quad (13)$$

Subject to:

$$\begin{aligned} \forall i \in \{0, \dots, n-1\} \quad \forall k \in \{0, \dots, H/T_i - 1\} \\ k \cdot T_i \leq s_{i,k} \leq k \cdot T_i + D_i - C_i \end{aligned} \quad (13a)$$

$$\text{ResourceBound}(s) = 0 \quad (13b) \quad 426$$

$$\forall i \in \{0, \dots, 2N-2\}, \text{Time}(\mathcal{O}(i)) \leq \text{Time}(\mathcal{O}(i+1)) \quad 427$$

$$(13c) \quad 428$$

where the objective function  $\mathcal{F}(s)$  could be, for example, data age (4), reaction time (5), or time disparity (8).

*Theorem 4:* The constraints from a job order  $\mathcal{O}$  simplify the problem (13) into a convex problem, specifically, an LP problem, when the optimization objective is DARTD.

*Proof:* Given a job order  $\mathcal{O}$ , the relative start/finish relationship of any two jobs is known, therefore all the job chains and job merges are decided. Then  $\mathbf{DA}(s)$  and  $\mathbf{RT}(s)$  become linear functions (lengths of all job chains in Definition 3). The  $\mathbf{TD}(s)$  can also be similarly transformed into linear functions following [26]. Constraints (13a) and (13c) are evidently linear functions. As for the computational resource bounds (13b) from the IO test (9), since the given job order  $\mathcal{O}$  already specifies the relative order of all the job scheduling times, (9) becomes linear inequalities. Therefore, problem (13) is an LP problem. ■

Next, we use  $\pi^*(\mathcal{O})$  to denote the optimal schedule for the problem (13). Note that the  $\pi^*(\mathcal{O})$  depends on the specific forms of objective functions and constraints.

*Definition 9 (Optimal Job Order Schedule):* The optimal job order schedule,  $s^* = \pi^*(\mathcal{O}) = \text{argmin}_s \mathcal{F}(s)$ , is the optimal solution of the optimization problem (13).

*Example 6:* In Example (1), consider a job order:  $\mathcal{O} = \{\mathcal{T}_{0,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{1,0}^s, \mathcal{T}_{1,0}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f\}$ , where we assume  $J_{0,0}$  and  $J_{1,0}$  are assigned to one processor  $\mathcal{P}_0$ , while  $J_{2,0}$  and  $J_{0,1}$  are assigned to another processor  $\mathcal{P}_1$ . Next, consider optimizing the reaction time of a cause-effect chain  $\mathcal{C} = \{\tau_0 \rightarrow \tau_2\}$ . The problem (13) can be transformed into an LP problem as follows:

$$\text{Minimize}_s \max \{f_{2,0} - s_{0,0}, f_{2,1} - s_{0,1}\} \quad (14) \quad 458$$

Subject to: 459

$$f_{0,0} = s_{0,0} + C_0, \quad f_{0,1} = s_{0,1} + C_0 \quad (14a) \quad 460$$

$$f_{1,0} = s_{1,0} + C_1, \quad f_{2,0} = s_{2,0} + C_2 \quad (14b) \quad 461$$

$$f_{2,1} = s_{2,0} + H + C_2 \quad (14c) \quad 462$$

$$0 \leq s_{0,0} \leq D_0 - C_0, \quad T_0 \leq s_{0,1} \leq T_0 + D_0 - C_0 \quad (14d) \quad 463$$

$$0 \leq s_{1,0} \leq D_1 - C_1, \quad 0 \leq s_{2,0} \leq D_2 - C_2 \quad (14e) \quad 465$$

$$f_{1,0} - s_{0,0} \geq C_0 + C_1, \quad f_{0,1} - s_{2,0} \geq C_0 + C_2 \quad (14f) \quad 467$$

$$s_{0,0} \leq s_{0,0} + C_0 \leq s_{1,0} \leq s_{1,0} + C_1 \leq s_{2,0} \quad (14g) \quad 468$$

$$s_{2,0} \leq s_{2,0} + C_2 \leq s_{0,1} \leq s_{0,1} + C_0. \quad (14h) \quad 469$$

The objective function (14) considers the length of two job chains initiated by  $J_{0,0}$  and  $J_{0,1}$  within a hyper-period. Constraints (14a)–(14c) are due to the nonpreemptive scheduling. Constraints (14d) and (14e) are schedulability constraints. Inequalities (14f) are the resource bound (13b). There are only two IO-test constraints because jobs assigned to different processors can overlap. Constraints (14g) and (14h) posed by the given job order.

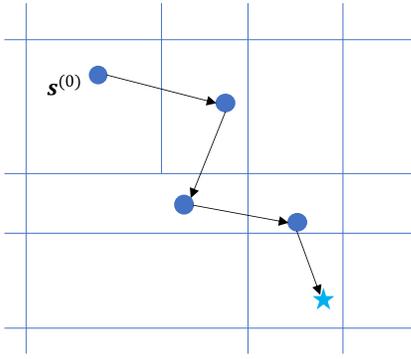


Fig. 3. TOM intuition. The solution space is divided into multiple “subspaces,” and the optimal solution within each subspace can be found efficiently by solving an LP problem. This process is visualized above: each job order defines a convex subspace (because all the constraints are linear after specifying a job order) and is informally visualized as a grid in the figure above. The optimal solution within each grid is denoted as a solid circle. The original optimization problem, which needs to explore the whole solution space, is simplified into evaluating only the optimal solutions within each subspace.

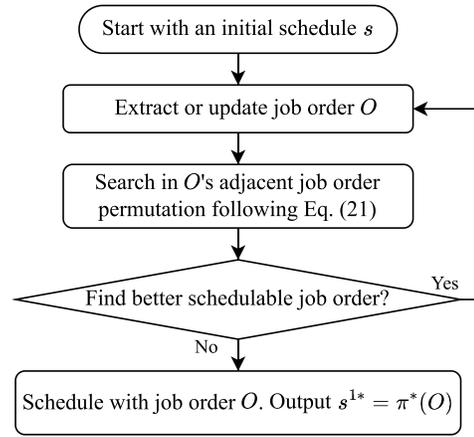


Fig. 4. Main optimization framework. We begin with an initial feasible solution  $s$  and its job order  $\mathcal{O}$ . Then in each iteration, we search for a better job order in  $\mathcal{O}$ 's adjacent job order permutation  $\mathcal{B}(\mathcal{O})$  and update the best job order found yet. Eventually, the iteration will terminate at a 1-opt solution.

478 *Definition 10 (Schedulable Job Order):* A job order  $\mathcal{O}$  is  
479 schedulable if there exists a schedulable schedule  $s$  that also  
480 satisfies the job order (13c).

## 481 V. TWO-STAGE OPTIMIZATION SCHEDULING

482 Although finding the optimal schedule given a job order  
483 is simple and efficient, enumerating all the possible job  
484 orders naively requires high-computation costs. Therefore, we  
485 propose an iterative algorithm, two-stage optimization method  
486 (TOM), to search for better job orders. TOM is proven to find  
487 1-opt solutions.

### 488 A. Optimization Concepts Review

489 *Definition 11 (Global Optimality):* A solution  $s^*$  for the  
490 problem (1) is global optimal if there is no other feasible  
491 solutions  $s$  such that  $\mathcal{F}(s) < \mathcal{F}(s^*)$ .

492 *Definition 12 (Local Optimality):* A solution  $s^*$  for the  
493 problem (1) is local optimal if there exists a small number  
494  $\delta > 0$ , such that there is no other feasible solutions  $s \in \mathcal{B}(s^*)$   
495 where  $\mathcal{F}(s) < \mathcal{F}(s^*)$ ,  $\mathcal{B}(s^*) = \{s \mid \|s - s^*\| \leq \delta\}$ .

496 *Definition 13 (1-opt, [11], [12]):* A solution  $s^{1*}$  for the  
497 problem (1) is 1-opt if “the objective value at  $s^{1*}$  does not  
498 improve by changing a single coordinate,” i.e.,  $\mathcal{F}(s^{1*}) \leq$   
499  $\mathcal{F}(s^{1*} + \mathbf{e}_i c)$  for arbitrary unit vector  $\mathbf{e}_i = \{0, \dots, 1, \dots, 0\}$   
500 and  $c \neq 0$ .

501 Although a global optimal solution is also local optimal  
502 and 1-opt, local optimal and 1-opt solutions are not inclusive  
503 of each other. In many real-time system problems, achieving  
504 global optimal or even local optimal solutions within reason-  
505 able time limits is difficult. In these cases, 1-opt provides a  
506 better tradeoff between optimality and run-time complexity.

### 507 B. Two-Stage Optimization Method

508 Due to the nonconvex and noncontinuous nature of  
509 problem (1), straightforward optimization algorithms neces-  
510 sitate an infinite number of objective function evaluations

to verify whether a solution is 1-opt. However, the concept 511  
of job order significantly simplifies the problem (1) and 512  
allows us to verify whether a solution is 1-opt with only 513  
polynomial time complexity. Therefore, we propose a two- 514  
stage optimization method (TOM). Fig. 4 shows an overview 515  
of TOM. Starting from an initial feasible schedule, the first 516  
stage searches for better job orders based on an iterative 517  
algorithm, while the second stage finds the optimal schedule 518  
by solving problem (13) for each job order to evaluate. 519

### 520 C. Theorems on 1-opt Conditions

521 *Definition 14 (Adjacent Schedule Permutation):* The adja- 522  
cent schedule permutation  $\mathcal{B}(s)$  of a schedule  $s$  is a set of 523  
schedules, where each schedule  $\mathcal{B}(s)_l$  differs from  $s$  by only 524  
one job’s start time.

525 *Definition 15 (Adjacent Job Order Permutation):* Adjacent 526  
job order permutation  $\mathcal{B}(\mathcal{O})$  of a job order  $\mathcal{O}$  is a finite set of 527  
distinct job orders. For each job order  $\mathcal{B}(\mathcal{O})_l$ , there is one and 528  
only one job  $J_{i,k}$  that the position of its scheduling start time 529  
 $\mathcal{T}_{i,k}^s$ , or its scheduling finish time  $\mathcal{T}_{i,k}^f$ , or both, are different 530  
from those in  $\mathcal{O}$ . The relative order of all the other jobs’ 531  
scheduling time in  $\mathcal{O}$  and  $\mathcal{B}(\mathcal{O})_l$  remain the same.

532 *Example 7:* Following Example 1, let us consider a 533  
job order  $\mathcal{O} = \{\mathcal{T}_{0,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{1,0}^s, \mathcal{T}_{1,0}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f\}$ . As 534  
an example,  $\mathcal{B}(\mathcal{O})$  could include an job order such as 535  
 $\{\mathcal{T}_{0,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f, \mathcal{T}_{1,0}^s, \mathcal{T}_{1,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f\}$  by moving  $J_{1,0}$  to the 536  
end of  $J_{2,0}$ . An alternative adjacent job order could be 537  
 $\{\mathcal{T}_{0,0}^s, \mathcal{T}_{1,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{1,0}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f\}$  where  $\mathcal{T}_{0,0}^f$  is moved to 538  
the back of  $\mathcal{T}_{1,0}^s$ , which means  $J_{1,0}$  will start execution before 539  
 $J_{0,0}$  finishes. It is schedulable if there is more than 1 processor.

540 *Theorem 5:* Consider a schedule  $s^{1*}$  and its job order  $\mathcal{O}^{1*}$ .  
541  $s^{1*}$  is a 1-opt solution for the optimization problem (1) if it  
542 satisfies the following conditions:

$$\mathcal{O}^{1*} = \operatorname{argmin}_{\mathcal{O} \in \mathcal{B}(\mathcal{O}^{1*}) \cap \Omega} \mathcal{F}(\pi^*(\mathcal{O})) \quad (15) \quad 543$$

$$s^{1*} = \pi^*(\mathcal{O}^{1*}) \quad (16) \quad 544$$

545 where  $\pi^*(\mathcal{O})$  denotes the optimal schedule obtained by  
 546 solving the problem (13) for  $\mathcal{O}$  and  $\Omega$  denotes the set of  
 547 schedulable job orders following Definition 10.

548 *Proof:* Consider an arbitrary solution  $\hat{s}$  which differs from  
 549  $s^{1*}$  by only one job's start time, and denote the job order of  
 550  $\hat{s}$  as  $\hat{\mathcal{O}}$ . In the case, we can introduce a function  $\pi(\cdot)$  which  
 551 obtains the schedule  $\hat{s} = \pi(\hat{\mathcal{O}})$ .  $\pi(\cdot)$  is possibly different from  
 552  $\pi^*(\cdot)$  in Definition 9. Following Definition 15, we know  $\hat{\mathcal{O}} \in$   
 553  $\mathcal{B}(\mathcal{O}^{1*})$ , and therefore

$$554 \quad \mathcal{F}(\hat{s}) = \mathcal{F}(\pi(\hat{\mathcal{O}})) \geq \mathcal{F}(\pi^*(\mathcal{O}^{1*})) = \mathcal{F}(s^{1*}). \quad (17)$$

555 Therefore,  $s^{1*}$  is 1-opt.  $\blacksquare$

556 *Example 8:* Let us continue with Example 1 and con-  
 557 sider the reaction time optimization problem of a chain  
 558  $\mathcal{C} = \{\tau_0 \rightarrow \tau_2\}$ . A 1-opt schedule could be  $s^{1*} =$   
 559  $[s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}] = [9, 10, 18, 11]$ . This solution is 1-opt  
 560 because there is no better feasible solution if only changing  
 561 one job's start time while leaving the other three jobs' start  
 562 times unchanged.

563 *Lemma 1:* If there are six variables which satisfy  $a_1 + c_1 \leq$   
 564  $b_1$ ,  $b_2 + c_2 \leq a_2$ , then  $\max(|a_1 - a_2|, |b_1 - b_2|) \geq \min(c_1, c_2)$ .

565 *Proof:* Prove by contradiction. Assume  $\max(|a_1 - a_2|, |b_1 -$   
 566  $b_2|) < \min(c_1, c_2)$ , then we have

$$567 \quad a_2 - a_1 < c_1, \quad b_1 - b_2 < c_2. \quad (18)$$

568 Combine with the theorem assumptions, we can derive

$$569 \quad a_2 < a_1 + c_1 \leq b_1, \quad b_1 < b_2 + c_2 \leq a_2. \quad (19)$$

570 The two inequalities above conflict with each other, therefore  
 571 the lemma is proven.  $\blacksquare$

572 *Theorem 6:* Assume each job has a nonzero execution time  
 573 and is executed in single-core systems nonpreemptively. Any  
 574 schedule  $s$  obtained by solving the LP problem (13) is local  
 575 optimal.

576 *Proof:* Prove by contradiction. Assume  $s$  is not a local  
 577 optimal solution. This implies the existence of another feasible  
 578 solution  $s^*$  such that  $\mathcal{F}(s^*) < \mathcal{F}(s)$ , where  $\|s - s^*\| < \delta$ ,  
 579 and  $\delta > 0$  is a very small number. Denote the job order of  $s$   
 580 and  $s^*$  as  $\mathcal{O}$  and  $\mathcal{O}^*$ , respectively. Then we must have  $\mathcal{O}^* \neq$   
 581  $\mathcal{O}$  because  $s$  is optimal for the problem (13) given the job  
 582 order  $\mathcal{O}$ .

583 Since we are considering a nonpreemptive single-core plat-  
 584 form, no jobs can run in parallel. Furthermore, since the job  
 585 orders are different, there must exist at least two jobs  $J_{i,k}$  and  
 586  $J_{j,l}$ , whose relative execution order is different. Without loss  
 587 of generality, assume  $J_{i,k}$  runs earlier than  $J_{j,l}$  in  $\mathcal{O}$ , and  $J_{j,l}$   
 588 runs earlier in  $\mathcal{O}^*$ . Mathematically speaking, that means

$$589 \quad s_{i,k} + C_i \leq s_{j,l}, \quad s_{j,l}^* + C_j \leq s_{i,k}^*. \quad (20)$$

590 Based on Lemma 1, we have  $\max(|s_{i,k} - s_{i,k}^*|, |s_{j,l} - s_{j,l}^*|) \geq$   
 591  $\min(C_1, C_2)$ . Therefore,  $\|s - s^*\| \geq \max(|s_{i,k} - s_{i,k}^*|, |s_{j,l} -$   
 592  $s_{j,l}^*|) \geq \min(C_1, C_2) > \delta$ , which causes a contradiction.  
 593 Therefore, the theorem is proved.  $\blacksquare$

594 Thus, the 1-opt schedule  $s^{1*}$  from Theorem 5 for a nonpre-  
 595 emptive single-core system is also local optimal.

## D. Optimization Algorithm Toward 1-opt Schedules

596

Following Theorem 5, we can design a simple algorithm  
 to search for better job orders iteratively. The algorithm will  
 update the job order following (21) and terminate when the  
 iterations converges, i.e.,  $\mathcal{O}^{(k+1)} = \mathcal{O}^{(k)}$ :

$$601 \quad \mathcal{O}^{(k+1)} = \underset{\mathcal{O} \in \mathcal{B}(\mathcal{O}^{(k)}) \cap \Omega}{\operatorname{argmin}} \mathcal{F}(\pi^*(\mathcal{O})) \quad (21)$$

where  $\pi^*(\mathcal{O})$  is the optimal job order schedule of  $\mathcal{O}$   
 and  $\Omega$  denotes the set of schedulable job orders following  
 Definition 10.

*Theorem 7:* An iterative algorithm that updates the job  
 order variables following (21) will terminate after a finite  
 number of iterations, and the solution found is 1-opt.

*Proof:* The iterative algorithm will terminate after a finite  
 number of iterations because a new iteration is initiated only  
 after finding a feasible, better solution in previous iterations.  
 Considering that the optimal objective function value is pos-  
 itive, the algorithm is guaranteed to terminate after a finite  
 number of iterations. When the algorithm terminates, the two  
 conditions in Theorem 5 are both satisfied and therefore the  
 solution is 1-opt.  $\blacksquare$

## VI. ENHANCING TOM—STRATEGIES FOR IMPROVED PERFORMANCE AND EFFICIENCY

### A. Skipping Unschedulable Job Orders

618

Although the feasibility of a job order can be analyzed by  
 solving the LP problem in problem (13), the average run-  
 time complexity is  $O(N^{2.5})$  [33]. Therefore, we propose the  
 following lightweight lemma to quickly examine whether a  
 job order is schedulable with  $O(N)$  complexity. These lemmas  
 are necessary, but not sufficient, conditions of schedulability:

*Lemma 2:* Given a job order  $\mathcal{O}$ , if there exists one job  $J_{i,k}$   
 whose scheduling finish time  $\mathcal{T}_{i,k}^f$  precedes its scheduling start  
 time  $\mathcal{T}_{i,k}^s$ , then  $\mathcal{O}$  is not schedulable.

*Lemma 3:* Given a job order  $\mathcal{O}$ , if the maximum number of  
 concurrent jobs exceeds the total number of processors, then  
 $\mathcal{O}$  is not schedulable.

Proofs of these lemmas are straightforward as they breach  
 either (13a) or (13b).

### B. More Relaxed Constraints in LP

633

The solution quality of an optimization problem could  
 become better if its constraints are relaxed. In problem (13),  
 although we cannot relax (13a) and (13b) (hard schedulability  
 constraints), we can relax the job order (13c) because it is only  
 necessary to maintain the relative order of jobs that influence  
 the objective functions (because not all the tasks contribute  
 to the cause-effect chains or merges) to guarantee that the  
 objective functions can be equivalently transformed into linear  
 functions.

*Example 9:* Continue with Example 1, given a job order  
 $\mathcal{O} = \{\mathcal{T}_{0,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{1,0}^s, \mathcal{T}_{1,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f\}$ , suppose we  
 only have one processor and want to optimize the reaction  
 time of the cause-effect chain  $\mathcal{C} = \{\tau_0 \rightarrow \tau_2\}$ . In this  
 case, the optimal schedule  $\pi^*(\mathcal{O}) = [s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}] =$   
 $[7, 10, 8, 11]$ , the worst-case reaction time is 7 from the job

**Algorithm 1: Simple Job Order Scheduler**


---

**Input:** Job order  $\mathcal{O}$   
**Output:** Schedule  $s$

```

1  $t = 0$  // Record current time
2 for each  $\mathcal{T}_i$  in  $\mathcal{O}$  do
3    $J_i = \text{GetJob}(\mathcal{T}_i)$ 
4   if  $\mathcal{T}_i$  is job scheduling start time then
5      $t = \max(t, J_i.\text{release\_time},$ 
6        $\text{NextProcessorAvailableTime}())$ 
7      $s_i = t$ 
8   else
9     if  $s_i + C_i \leq t$  then
10       $t = s_i + C_i, f_i = s_i + C_i$ 
11    else
12      return  $\mathbf{0}$  //  $\mathcal{O}$  is unschedulable
13    end
14  end
15 return  $s$ 

```

---

chain  $\{J_{0,0} \rightarrow J_{2,0}\}$ . Since  $J_{1,0}$  does not influence the length of the cause-effect chain  $\mathcal{C} = \{\tau_0 \rightarrow \tau_2\}$ , only enforcing the relative job order among  $\{J_{0,0}, J_{0,1}, J_{2,0}\}$  is enough to transform the objective function (5) into linear functions. Then the optimal schedule with relaxed constraints become  $s^{\text{relaxed}} = [s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}] = [9, 10, 0, 11]$ . The worst-case reaction time is reduced to 5.

### C. Simple Job Order Scheduler

In cases when the run-time complexity becomes a major performance bottle-neck, we can use a heuristic scheduling algorithm with  $O(N)$  complexity to replace solving the LP problem (13) that usually requires  $O(N^{2.5})$  time complexity [33]. The simple job order scheduler adopts a First-In-First-Out scheduling policy. A job becomes ready for execution after satisfying two conditions: 1) its release time has passed and 2) its previous job scheduling time has happened. Algorithm 1 shows the pseudocode of the simple job order scheduler in a simulation environment.

*Example 10:* Continue with Example 9, consider the same job order  $\mathcal{O} = \{\mathcal{T}_{0,0}^s, \mathcal{T}_{0,0}^f, \mathcal{T}_{1,0}^s, \mathcal{T}_{1,0}^f, \mathcal{T}_{0,1}^s, \mathcal{T}_{0,1}^f, \mathcal{T}_{2,0}^s, \mathcal{T}_{2,0}^f\}$ . If there is only one computation core, the schedule obtained from the simple order scheduler is  $[s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}] = [0, 10, 1, 11]$ . In case of two cores, the schedule is  $[s_{0,0}, s_{0,1}, s_{1,0}, s_{2,0}] = [0, 10, 0, 10]$ .

Despite its fast speed, the simple job order scheduler suffers from two major disadvantages: 1) nonexact schedulability analysis and 2) nonoptimal schedule without any theoretical guarantee. It is only encouraged to use if solving the problem (13) iteratively suffers from a big time-out issue.

## VII. IMPLEMENTATION DETAILS

### A. Initial Solution Estimation

In the experiments, we use a simple list-scheduling method [13] to obtain an initial schedule. If multiple jobs

**Algorithm 2: Single Iteration of TOM**


---

**Input:** Job order  $\mathcal{O}^{(k)}$ , job set  $\mathbf{J}$  containing all jobs in a hyper-period  
**Output:**  $\mathcal{O}^{(k+1)}$

```

1  $\mathcal{O}^{\text{tmp}} = \mathcal{O}^{(k)}$ 
2 for each job  $J_i$  in  $\mathbf{J}$  do
3   for each job order  $\mathcal{O}$  in  $\mathcal{B}^{J_i}(\mathcal{O}^{\text{tmp}})$  do
4     if  $\mathcal{F}(\pi^*(\mathcal{O})) < \mathcal{F}(\pi^*(\mathcal{O}^{\text{tmp}}))$  then
5        $\mathcal{O}^{\text{tmp}} = \mathcal{O}$ 
6     end
7   end
8 end
9  $\mathcal{O}^{(k+1)} = \mathcal{O}^{\text{tmp}}$ 
10 return  $\mathcal{O}^{(k+1)}$ 

```

---

become ready, jobs with the least finish time will be dispatched first. The processor assignments are decided based on a simple First-Come-First-Serve strategy. In practice, other methods can also be used to obtain a feasible initial schedule.

### B. Faster Implementation Within Time Limits

TOM is implemented slightly differently from (21) for faster run-time efficiency. When searching for an optimal job order  $\mathcal{O}^{(k)*}$  within  $\mathcal{B}(\mathcal{O}^{(k)})$ , we immediately accept a new job order  $\mathcal{O}$  if it improves  $\mathcal{O}^{(k)}$ . Algorithm 2 shows the pseudocode of one single iteration. In line 3,  $\mathcal{B}^{J_i}(\mathcal{O}^{\text{tmp}})$  denotes the adjacent job order permutation of  $\mathcal{O}^{\text{tmp}}$  by only changing the index of  $J_i$ 's job scheduling time.  $\mathcal{O}^{\text{tmp}}$  will be updated if a better job order is found. Following Theorem 7, Algorithm 2 also finds 1-opt solutions after algorithm termination.

### C. When to Assign Processor

A simple first-come-first-serve (FCFS) policy is used for processor assignment for each job. In experiments, we utilize the simple job order scheduler (Section VI-C) to generate the processor assignment before evaluating a job order (i.e., solving problem (13)). After obtaining the processor assignments, we formulate the resource-bound constraints for problem (13).

### D. Worst-Case Complexity Analysis

The overall algorithm's complexity depends on the complexity of each iteration and the total number of iterations. In the experiments, TOM usually terminates in less than 10 iterations. Following (21), the cost of each iteration depends on the number of job orders to search and the cost to evaluate a single job order [problem (13)]. In the worst case, the total number of adjacent job order permutations could be  $O(N^3)$ . However, techniques from Section VI-A can greatly reduce the possible permutations. Evaluating a single job order has two steps: 1) obtaining a schedule and 2) then evaluating the objective function. The former could be as fast as  $O(N)$  if a simple job order scheduler is used. In terms of solving the linear program, the complexity could increase to  $O(N^{2.5})$  in average case [33] (in reality, since problem (13) is very sparse, the real run-time speed should be much faster than  $O(N^{2.5})$ ).

719 Finally, evaluating the objective function given a schedule  
720 requires  $O(N^2)$  complexity in worst cases.

721 Overall, the worst-case complexity in one iteration is  $O(N^3 \cdot$   
722  $(N^{2.5} + N^2))$  if an optimal job order scheduler [solving  
723 problem (13)] is used. However, most experiments finish  
724 optimizing task sets of thousands of jobs within 1000 s, which  
725 suggests the average time complexity to be  $O(N^4)$ .

## 726 VIII. EXTENSIONS AND LIMITATIONS

727 This section briefly discusses several possible extensions  
728 and leaves the experiment verification to future works.

### 729 A. Alternative Objective Functions

730 Apart from the objective functions shown in Sections III-C  
731 and III-D, TOM also supports other forms of objective  
732 functions, such as linear combination of DARTD. Besides,  
733 TOM can also optimize nonlinear functions of different timing  
734 metrics (such as jitters of end-to-end latency) and solve them  
735 with nonLP methods [10], [34], though without the 1-opt or  
736 local-optimal guarantee anymore.

### 737 B. Extension For Preemptive Scheduling

738 While the TOM framework is designed for nonpreemptive  
739 TTS systems, it can be extended to work with preemptive  
740 systems. First, similar to the start time variables, an extra  
741 set of finish time variables has to be incorporated into the  
742 optimization problem formulation. The schedulability analysis  
743 constraints (Section III-E) have to be replaced with the demand  
744 bound function used in [32]. The concept of job order remains  
745 the same because it already incorporates the finish time.

### 746 C. Finding Feasible Initial Schedules

747 The TOM optimization framework can also be utilized  
748 to find feasible schedules. This section briefly discusses the  
749 theoretical foundations. Since feasibility is a binary metric  
750 that is not friendly for optimization, we utilize ‘‘tardiness’’  
751 as the optimization objective function (similar to [10]). The  
752 feasibility optimization problem is formulated as follows:

$$753 \quad \text{Minimize}_s \sum_{i=0}^{n-1} \sum_{k=0}^{H/T_i-1} \text{Barrier}(kT_i + D_i - C_i - s_{i,k}) \quad (22)$$

$$754 \quad \text{Barrier}(x) = \begin{cases} 0, & x \geq 0 \\ -x, & x < 0 \end{cases} \quad (22a)$$

755 Subject to:

$$756 \quad \forall i \in \{0, \dots, n-1\} \quad \forall k \in \{0, \dots, H/T_i-1\}, kT_i \leq s_{i,k} \quad (22b)$$

$$758 \quad \text{ResourceBound}(s) = 0. \quad (22c)$$

759 *Theorem 8:* If a solution  $s$  can reduce the objective func-  
760 tion in problem (22) into 0 while also being feasible for  
761 problem (22), then  $s$  is a schedulable schedule.

762 *Proof:* If the objective function is reduced to 0, no jobs  
763 violate the deadline constraints. Combined with the job release  
764 (22b) and processor overloading (22c), the schedule  $s$  is  
765 schedulable by definition. ■

*Theorem 9:* List scheduling can always provide a feasible  
initial solution to problem (22). 766

*Proof:* The schedule found by list scheduling is always  
feasible for problem (22) because a job is dispatched for  
execution whenever there is an idle processor [satisfying (22c)]  
after the job is released in (22b). ■ 771

*Theorem 10:* The problem (22) can be equivalently trans-  
formed into an LP problem after adding an extra set of job  
order constraints [the inequality (13c)]. 774

*Proof:* Following Theorem 4, we only need to prove that  
the objective function (22) can be transformed into linear  
functions. This can be easily done by introducing an artificial  
variable  $z_{i,k}$  for each term following [26]. After that, the  
objective function becomes 779

$$\text{Minimize}_s \sum_{i=0}^{n-1} \sum_{k=0}^{H/T_i-1} z_{i,k} \quad (23) \quad 780$$

with extra linear constraints 781

$$\begin{aligned} 782 \quad & \forall i \in \{0, \dots, n-1\} \quad \forall k \in \{0, \dots, H/T_i-1\} \\ 783 \quad & z_{i,k} \geq 0 \quad \& \quad z_{i,k} \geq -1 \cdot (kT_i + D_i - C_i - s_{i,k}). \end{aligned} \quad (24)$$

Since both the objective functions and the constraints are linear  
functions after transformation, the theorem is proved. ■ 785

The theorems above show that TOM can also solve the  
feasibility problem (22). It is also guaranteed to perform better  
than simple scheduling heuristics, such as list scheduling,  
because TOM utilizes them as initial solutions. 789

### 790 D. Limitations

Compared with global optimality, 1-opt provides a weaker  
form of theoretical guarantee. However, in general cases,  
obtaining global optimal solutions requires significantly higher  
computation costs. Therefore, given the same computation  
costs, 1-opt could potentially achieve better performance, as  
shown in our experiments. 796

TOM’s computation cost depends on the number of jobs  
within a hyper-period. Therefore, there could be a higher com-  
putation cost in nonharmonic task sets. However, in realistic  
TTS systems [35], there cannot be too many jobs within a  
hyper-period because that would incur a high overhead in task  
management and scheduling. Therefore, it is expected that the  
computation cost associated with TOM should be reasonably  
low in real-world systems. 804

## 805 IX. EXPERIMENT

The proposed framework was implemented in C++ and  
tested on a computing cluster (AMD EPYC 7702 CPU). We  
consider the following methods in experiments. 808

- 1) *List Scheduling [13]:* Whenever there are available  
processors, it dispatches the ready job with the least  
finish time for execution. 811
- 2) *Simulated Annealing [36]:* A general heuristic method  
for optimization problems. The initial temperature is  
 $1e8$ , and the cooling rate is 0.99, which encourages  
the algorithm to explore the solution space. The initial 815

816 schedule is obtained from the list scheduling, the same  
817 as TOM.

- 818 3) *Verucchi20* [7]: It was proposed to minimize the worst-  
819 case data age and reaction time in multirate DAG.  
820 The code implementation is adopted from their official  
821 release repository. If it does not run time out, its solution  
822 quality is close to the optimal solutions. To the best of  
823 our knowledge, it is also the most recent state-of-the-art  
824 work that considers a similar problem setting.
- 825 4) *TOM*: The optimization framework proposed in this  
826 article. When solving problem (13), CPLEX [37] is used  
827 to find optimal solutions.
- 828 5) *TOM\_SimpleScheduler*: Similar to TOM, except that the  
829 simple job order scheduler (Section VI-C) instead of LP  
830 is used when obtaining a schedule from a job order.
- 831 6) *TOM\_Extended*: Similar to TOM, except that we also  
832 enabled the relaxations on the LP problem's constraints,  
833 which is introduced in Section VI-B.

834 If one method runs time-out without a feasible solution, we  
835 use the results of list scheduling during the result analysis.

#### 836 A. Task Set Generation and Results

837 The simulated DAG task sets are generated following a real-  
838 world automotive benchmark [9], all the tasks' periods are  
839 randomly generated from a limited set {1, 2, 5, 10, 20, 50, 100,  
840 200, 1000}, with relative probability distribution: {3, 2, 2, 25,  
841 25, 3, 20, 1, 4}. The overall task set's utilization is set to 0.9  $m$ ,  
842 where  $m$  is the number of cores available, 4 in our experiments.  
843 Each task's WCET is generated by UUnifast [38] while  
844 following the multicore adaptation implementation in [10].  
845 Each task's relative deadline is the same as its period. Task  
846 sets generated in this way usually have hundreds or *thousands*  
847 of jobs to schedule.

848 Task dependencies are generated randomly following  
849 He et al. [39]. After generating individual tasks, we go through  
850 each pair of tasks and randomly add an edge from one task  
851 to another with a given probability, 0.9 in our experiments  
852 (smaller probabilities are usually insufficient to generate many  
853 cause-effect chains in the DAG). The number of tasks in a  
854 task set ranges from 5 to 20. Cause-effect chains are generated  
855 as the paths between random pairs of tasks using the shortest  
856 path algorithm in Boost Graph Library [40]. Task merges are  
857 generated by randomly selecting a sink task and then collecting  
858 all source tasks on which the sink task directly depends.

859 For a task set with  $n$  tasks, there are  $n$  to  $2n$  random  
860 cause-effect chains and  $[0.25n]$  to  $n$  random task merges. The  
861 maximum number of source tasks in a merge varies from 2  
862 to 9 following ROS [16]. The lengths and activation patterns  
863 of the cause-effect chains adhere to distributions outlined in  
864 Tables VI and VII of the automotive benchmark [9]. To meet  
865 distribution criteria, we initially generate plenty of task sets,  
866 evaluate the likelihood for each task set, and then sample 1000  
867 random task sets weighted by the likelihood for each given  
868 number of tasks. All task sets are schedulable under the list  
869 scheduling method. The run-time limit for scheduling one task  
870 set is 1000 s per method.

871 We tested the performance of each method in optimizing  
872 DARTD separately. The experiment results are reported in  
873 Fig. 5. All performance gaps are compared against the list  
874 scheduling method

$$\frac{\mathcal{F}_{\text{method}} - \mathcal{F}_{\text{List\_Scheduling}}}{\mathcal{F}_{\text{List\_Scheduling}}} \times 100\%. \quad (25) \quad 875$$

#### 876 B. Result Analysis and Discussion

877 Overall, TOM and its extensions significantly outperform  
878 other methods in various experiments. Next, we provide a  
879 more detailed analysis of different aspects.

880 1) *Comparison With Baseline Methods*: Compared with  
881 other baseline methods, the performance improvements of  
882 TOM and TOM\_Extended are not obvious when the number  
883 of tasks is small ( $n = 5$ ). This is because the solution space  
884 is very small and most methods can find good solutions.  
885 However, as the number of tasks increases, Verucchi20 quickly  
886 reaches time limits and can barely find schedulable schedules  
887 or schedules with low-end-to-end latency. Simulated annealing  
888 always starts its iteration with a feasible schedule. However,  
889 due to its inefficient solution space exploration techniques, it  
890 usually requires a long time to find a good solution, which  
891 often exceeds the given time limit and therefore cannot show  
892 much performance improvement. In contrast, guided by 1-opt,  
893 TOM and TOM\_Extended are able to explore the solution  
894 space efficiently while still maintaining good solution quality.  
895 These experiment results show the benefits of both 1-opt  
896 optimality and the proposed TOM optimization algorithms.

897 2) *TOM Versus TOM\_SimpleScheduler*: The performance  
898 improvements of TOM against TOM\_SimpleScheduler show  
899 the benefits of the LP formulation. Compared with simple  
900 heuristics, such as list scheduling, LP explores a larger  
901 solution space, can find nonwork-conserving schedules, and  
902 thus achieves better solution quality. The disadvantage of the  
903 LP approach is the higher computation cost. To compensate  
904 for the extra computation costs, many heuristics are proposed  
905 in this article without sacrificing the theoretical guarantee,  
906 such as using fast necessary conditions to filter unschedulable  
907 job orders (Section VI-A), exploring the sparse structure in  
908 implementation (the resource bound constraints are sparse  
909 linear constraints). However, TOM\_SimpleScheduler could  
910 still be an option in situations with many tasks/jobs.

911 3) *TOM Versus TOM\_Extended*: The performance  
912 improvements of TOM\_Extended against TOM show the  
913 effectiveness of the heuristics (Section VI-B) to further  
914 improve upon 1-opt while maintaining a similar run-time  
915 speed. Since the results obtained from both TOM\_Extended  
916 and TOM are 1-opt (if not running time-out), it implies that  
917 there are potentially many 1-opt solution candidates with  
918 varying solution qualities in the whole solution space. If  
919 applicable, utilizing heuristics to further improve upon 1-opt  
920 solutions is beneficial.

921 4) *Time-Out Issue*: It is possible that TOM does not  
922 finish iterations before running time out. In these cases,  
923 TOM degrades into heuristic algorithms without a theoretical  
924 guarantee. However, the trend in Fig. 5 shows that running  
925 time-out does not seriously degrade the solution quality even

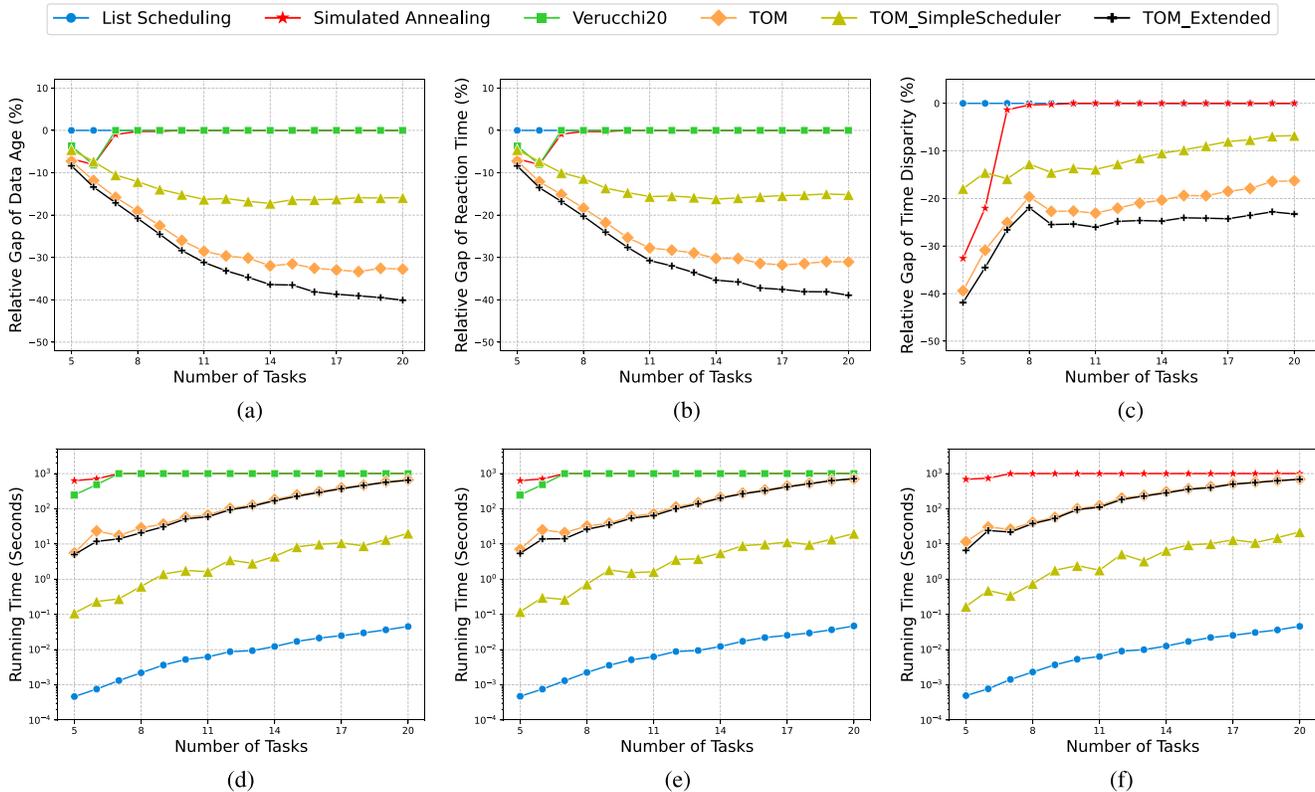


Fig. 5. Performance gap and running time for optimizing end-to-end latency and time disparity on synthetic task sets. (a) Data age performance. (b) Reaction time performance. (c) Time disparity performance. (d) Data age running time. (e) Reaction time running time. (f) Time disparity running time.

926 though more than 30% cases running time out when  $n = 20$   
 927 (around 4000 jobs per task set). We expect TOM to work  
 928 reasonably well for task sets with less than  $10^4$  jobs if  
 929 the time limit is 1000 s. Optimizing larger task sets, such  
 930 as those with  $10^5$  jobs, would require a much longer time  
 931 limit.

932 5) *Data Age Versus Reaction Time*: Experiments show that  
 933 data age and reaction time optimization have similar results.  
 934 Furthermore, reducing one metric usually reduces the other,  
 935 which is broadly consistent with the findings in [14]. This  
 936 observation may improve the algorithm efficiency in cases  
 937 where both data age and reaction time need to be optimized:  
 938 we may just consider only one metric in the objective function  
 939 and leave the other out.

### 940 C. Time Disparity Optimization Result

941 Although the overall results on time-disparity optimization  
 942 are good, Fig. 5(c) shows that the performance seems to  
 943 become worse when the number of tasks increases from 5  
 944 to 8. This is mainly due to the nature of the problem itself,  
 945 rather than the limitations of the optimizers. For example,  
 946 consider two merges where one merge has 2 source tasks and  
 947 1 sink task, and another merge has the same sink task, the  
 948 same 2 source tasks, and 2 more extra source tasks. In this  
 949 case, the maximum source time disparity of the second merge  
 950 could never become smaller than the first merge. In practice,  
 951 adding more source tasks does not necessarily make the list  
 952 scheduling perform worse after reaching certain limits, but

it does make the optimization more difficult, and limits the  
 performance improvements even for global optimal solutions.

## 955 X. CONCLUSION

956 In this article, we investigate a multirate DAG scheduling  
 957 problem to reduce the worst-case end-to-end latency and/or  
 958 time disparity metrics. Given the potentially vast number  
 959 of variables within the solution space, we advocate for  
 960 guiding the scheduling design with 1-opt. Our optimization  
 961 algorithm introduces a novel technique called *job order* to  
 962 partition the solution space into multiple convex subspaces.  
 963 This partitioning strategy allows utilizing LP to minimize  
 964 DARTD within each subspace. Building upon this parti-  
 965 tion, our algorithm iteratively traverses among the subspaces,  
 966 ensuring that the output is 1-opt. In contrast to alternative  
 967 optimization algorithms, such as meta-heuristics algorithms  
 968 lacking any theoretical performance guarantees, or optimal  
 969 algorithms that may require exponential run-time complexity,  
 970 the 1-opt algorithm balances the tradeoff between theoret-  
 971 ical performance guarantee and run-time complexity. We  
 972 rigorously prove that our optimization algorithm achieves  
 973 1-opt solutions while maintaining polynomial run-time com-  
 974 plexity. Further optimization heuristics are also proposed to  
 975 improve the algorithm's performance and efficiency without  
 976 compromising the 1-opt solution guarantee. Experimental  
 977 results indicate significant improvements over state-of-  
 978 the-art methods in both performance and computational  
 979 efficiency.

## REFERENCES

- 980
- 981 [1] N. Feiertag, K. Richter, J. Nordlander, and J. Jonsson, "A compositional  
982 framework for end-to-end path delay calculation of automotive systems  
983 under different path semantics," in *Proc. IEEE Real-Time Syst. Symp.*,  
984 2009, pp. 1–8.
- 985 [2] "2021 RTSS industry challenge." PerceptIn. 2021. [Online]. Available:  
986 <http://2021.rtss.org/industry-session/>
- 987 [3] I. S. Olmedo, N. Capodieci, J. L. Martinez, A. Marongiu, and  
988 M. Bertogna, "Dissecting the CUDA scheduling hierarchy: A  
989 performance and predictability perspective," in *Proc. IEEE Real-Time  
990 Embed. Technol. Appl. Symp.*, 2020, pp. 213–225.
- 991 [4] J. Abdullah, G. Dai, and W. Yi, "Worst-case cause–effect reaction  
992 latency in systems with non-blocking communication," in *Proc. Design,  
993 Autom. Test Europe Conf. Exhibit.*, 2019, pp. 1625–1630.
- 994 [5] M. Günzel, K.-H. Chen, N. Ueter, G. von der Brüggen, M. Dürr, and  
995 J.-J. Chen, "Timing analysis of asynchronized distributed cause–effect  
996 chains," in *Proc. IEEE 27th Real-Time Embed. Technol. Appl. Symp.*,  
997 2021, pp. 40–52.
- 998 [6] M. Dürr, G. von der Brüggen, K.-H. Chen, and J.-J. Chen, "End-  
999 to-end timing analysis of sporadic cause–effect chains in distributed  
1000 systems," *ACM Trans. Embed. Comput. Syst.*, vol. 18, pp. 1–24,  
1001 Oct. 2019.
- 1002 [7] M. Verucchi, M. Theile, M. Caccamo, and M. Bertogna, "Latency-  
1003 aware generation of single-rate DAGs from multi-rate task sets,"  
1004 in *Proc. IEEE Real-Time Embed. Technol. Appl. Symp.*, 2020,  
1005 pp. 226–238.
- 1006 [8] T. Klaus, M. Becker, W. Schröder-Preikschat, and P. Ulbrich,  
1007 "Constrained data-age with job-level dependencies: How to reconcile  
1008 tight bounds and overheads," in *Proc. IEEE 27th Real-Time Embed.  
1009 Technol. Appl. Symp.*, 2021, pp. 66–79.
- 1010 [9] S. Kramer, D. Ziegenbein, and A. Hamann, "Real world automotive  
1011 benchmarks for free," in *Proc. Int. Workshop Anal. Tools Methodol.  
1012 Embed. Real-Time Syst.*, 2015, pp. 1–6.
- 1013 [10] S. Wang, R. K. Williams, and H. Zeng, "A general and scalable  
1014 method for optimizing real-time systems with continuous variables,"  
1015 in *Proc. IEEE 29th Real-Time Embed. Technol. Appl. Symp.*, 2023,  
1016 pp. 119–132.
- 1017 [11] J. Park and S. Boyd, "A semidefinite programming method for integer  
1018 convex quadratic minimization," *Optim. Lett.*, vol. 12, pp. 499–518,  
1019 May 2018.
- 1020 [12] K. Khurshid, S. Irteza, A. A. Khan, and S. Shah, "Application of  
1021 heuristic (1-opt local search) and metaheuristic (ant colony optimization)  
1022 algorithms for symbol detection in MIMO systems," *Commun. Netw.*,  
1023 vol. 03, pp. 200–209, Nov. 2011. [Online]. Available: [https://api.  
1024 semanticscholar.org/CorpusID:44194350](https://api.semanticscholar.org/CorpusID:44194350)
- 1025 [13] H. R. Topcuoglu, S. Hariri, and M. Wu, "Performance-effective and low-  
1026 complexity task scheduling for heterogeneous computing," *IEEE Trans.  
1027 Parallel Distrib. Syst.*, vol. 13, no. 3, pp. 260–274, Mar. 2002.
- 1028 [14] M. Günzel, H. Teper, K.-H. Chen, G. von der Brüggen, and J.-J. Chen,  
1029 "On the equivalence of maximum reaction time and maximum data age  
1030 for cause–effect chains," in *Proc. Euromicro Conf. Real-Time Syst.*, 2023,  
1031 pp. 1–22. [Online]. Available: [https://api.semanticscholar.org/CorpusID:  
1032 259318171](https://api.semanticscholar.org/CorpusID:259318171)
- 1033 [15] J. Martinez, I. Sañudo, and M. Bertogna, "End-to-end latency char-  
1034 acterization of task communication models for automotive systems,"  
1035 *Real-Time Syst.*, vol. 56, pp. 315–347, Jul. 2020. [Online]. Available:  
1036 <https://api.semanticscholar.org/CorpusID:219935505>
- 1037 [16] R. Li, N. Guan, X. Jiang, Z. Guo, Z. Dong, and M. Lv, "Worst-case  
1038 time disparity analysis of message synchronization in ROS," in *Proc.  
1039 IEEE Real-Time Syst. Symp.*, 2022, pp. 40–52.
- 1040 [17] X. Jiang, X. Luo, N. Guan, Z. Dong, S. Liu, and W. Yi, "Analysis  
1041 and optimization of worst-case time disparity in cause–effect chains," in  
1042 *Proc. Design, Autom. Test Europe*, 2023, pp. 1–6.
- 1043 [18] S. Baruah, M. Bertogna, G. Buttazzo, S. Baruah, M. Bertogna, and  
1044 G. Buttazzo, "The sporadic DAG tasks model," in *Multiprocessor  
1045 Scheduling for Real-Time Systems*. Cham, Switzerland: Springer, 2015,  
1046 pp. 191–204.
- [19] A. Melani, M. Bertogna, V. Bonifaci, A. Marchetti-Spaccamela, and  
1047 G. C. Buttazzo, "Response-time analysis of conditional DAG tasks in  
1048 multiprocessor systems," in *Proc. Euromicro Conf. Real-Time Syst.*,  
1049 2015, pp. 211–221. 1050
- [20] J. Li, K. Agrawal, C. Lu, and C. D. Gill, "Analysis of global EDF  
1051 for parallel tasks," in *Proc. Euromicro Conf. Real-Time Syst.*, 2013,  
1052 pp. 3–13. 1053
- [21] J. Fonseca, G. Nelissen, and V. Nélis, "Improved response time analysis  
1054 of sporadic DAG tasks for global FP scheduling," in *Proc. Int. Conf.  
1055 Real-Time Netw. Syst.*, 2017, pp. 28–37. 1056
- [22] K. Tindell, A. Burns, and A. Wellings, "Allocating hard real-time  
1057 tasks: An NP-hard problem made easy," *Real-Time Syst.*, vol. 4,  
1058 pp. 145–165, 1992. [Online]. Available: [https://link.springer.com/article/  
1059 10.1007/BF00365407](https://link.springer.com/article/10.1007/BF00365407) 1060
- [23] Y. Zhao and H. Zeng, "The virtual deadline based optimization algorithm  
1061 for priority assignment in fixed-priority scheduling," in *Proc. IEEE Real-  
1062 Time Syst. Symp.*, 2017, pp. 116–127. 1063
- [24] Y. Zhao, R. Zhou, and H. Zeng, "An optimization framework for real-  
1064 time systems with sustainable schedulability analysis," in *Proc. IEEE  
1065 Real-Time Syst. Symp.*, 2020, pp. 333–344. 1066
- [25] J. Martinez, I. Sañudo, and M. Bertogna, "Analytical characterization  
1067 of end-to-end communication delays with logical execution time," *IEEE  
1068 Trans. Comput.-Aided Design Integr. Circuits Syst.*, vol. 37, no. 11,  
1069 pp. 2244–2254, Nov. 2018. 1070
- [26] S. Wang et al., "Optimizing logical execution time model for both  
1071 determinism and low latency," in *Proc. IEEE Real-Time Embed. Technol.  
1072 Appl. Symp.*, 2024, pp. 135–148. 1073
- [27] C. Bradatsch, F. Kluge, and T. Ungerer, "Data age diminution in the  
1074 logical execution time model," in *Proc. Int. Conf. Archit. Comput. Syst.*,  
1075 2016, pp. 173–184. 1076
- [28] L. Maia and G. Fohler, "Reducing end-to-end latencies of multi-rate  
1077 cause–effect chains in safety critical embedded systems," in *Proc. 12th  
1078 Eur. Congr. Embed. Real Time Softw. Syst. (ERTS)*, 2024, pp. 1–10. 1079
- [29] Y. Tang, X. Jiang, N. Guan, D. Ji, X. Luo, and W. Yi, "Comparing com-  
1080 munication paradigms in cause–effect chains," *IEEE Trans. Comput.*,  
1081 vol. 72, no. 1, pp. 82–96, Jan. 2023. 1082
- [30] F. Paladino, A. Biondi, E. Bini, and P. Pazzaglia, "Optimizing  
1083 per-core priorities to minimize end-to-end latencies," in *Proc. 1084  
36th Euromicro Conf. Real-Time Syst. (ECRTS)*, 2024, pp. 1–25. 1085  
[Online]. Available: [https://drops.dagstuhl.de/entities/document/10.4230/  
1086 LIPIcs.ECRTS.2024.6](https://drops.dagstuhl.de/entities/document/10.4230/LIPIcs.ECRTS.2024.6) 1087
- [31] A. Hamann, D. Dasari, S. Kramer, M. Pressler, and F. Wurst,  
1088 "Communication centric design in complex automotive embedded  
1089 systems," in *Proc. Euromicro Conf. Real-Time Syst.*, 2017, pp. 1–20. 1090
- [32] S. Baruah, "Scheduling DAGs when processor assignments are speci-  
1091 fied," in *Proc. Int. Conf. Real-Time Netw. Syst.*, 2020, pp. 111–116. 1092
- [33] M. B. Cohen, Y. T. Lee, and Z. Song, "Solving linear programs in the  
1093 current matrix multiplication time," *J. ACM*, vol. 68, no. 1, pp. 1–39,  
1094 2021. 1095
- [34] J. Nocedal and S. J. Wright, "Nonlinear equations," in *Numerical  
1096 Optimization*. New York, NY, USA: Springer, 2006, pp. 270–302. 1097
- [35] A. Minaeva and Z. Hanzálek, "Survey on periodic scheduling for  
1098 time-triggered hard real-time systems," *ACM Comput. Surveys*, vol. 54,  
1099 pp. 1–32, Mar. 2021. [Online]. Available: [https://api.semanticscholar.  
1100 org/CorpusID:233354222](https://api.semanticscholar.org/CorpusID:233354222) 1101
- [36] P. J. Van Laarhoven, E. H. Aarts, P. J. van Laarhoven, and E. H. Aarts,  
1102 *Simulated Annealing*. Dordrecht, The Netherlands: Springer, 1987. 1103
- [37] I. I. Cplex, "V12.1: User's manual for CPLEX," *Int. Bus. Mach. Corp.*,  
1104 vol. 46, no. 53, p. 157, 2009. 1105
- [38] E. Bini and G. Buttazzo, "Measuring the performance of schedulability  
1106 tests," *Real-Time Syst.*, vol. 30, pp. 129–154, May 2005. 1107
- [39] Q. He, M. Lv, and N. Guan, "Response time bounds for DAG tasks  
1108 with arbitrary intra-task priority assignment," in *Proc. Euromicro Conf.  
1109 Real-Time Syst.*, 2021, pp. 1–21. 1110
- [40] J. G. Siek, L.-Q. Lee, and A. Lumsdaine, *The Boost Graph Library-  
1111 User Guide and Reference Manual (C++-depth)*. Boston, MA, USA:  
1112 Addison-Wesley, 2001. 1113