

M5 Assignment M5-CS

BE SURE to watch the VIDEOS

Connect LaunchPad, download, and run
[sketch_CS_DCO_HW.ino](#)

Download, install and run the sketch

You should study the CS code and review the functions from the video

```
35) __bis_SR_register(SCG0);           // disable FLL
36) CSCTL3 |= SELREF__REFOCLK;        // Set REFOCLK as FLL reference source
37) CSCTL0 = 0;                       // clear DCO and MOD FLL registers
38) CSCTL1 &= ~(DCORSEL_7);           // Clear DCO frequency select bits first
39) CSCTL1 |= DCORSEL_3;              // Set DCOCLK = 8MHz
40) CSCTL2 = FLLD_1 + 121;            // FLLD = 1, FFLN=121, DCODIV = 4MHz
41) __delay_cycles(3);
42) __bic_SR_register(SCG0);          // enable FLL
43) while(CSCTL7 & (FLLUNLOCK0 | FLLUNLOCK1)); // Poll until FLL is locked
44) CSCTL4 = SELMS__DCOCLKDIV | SELA__REFOCLK; // set ACLK = XT1 = 32768Hz, DCOCLK as MCLK and
SMCLK source
45) CSCTL5 |= DIVM1;                  // SMCLK = MCLK = DCODIV/4 = 1MHz
46) //CSCTL5 |= DIVM1 | DIVS0;        // slow down /
```

DOCUMENT:

Modify the sketch code line 45 to include DIVS0 as shown above in line 46

Include a screen shot of the 'Successful' download of the code

Include a listing of the sketch code as run

Include a photo of your board with LED's illuminated

VIDEO

Show 'Successful' of download screen from your monitor

Show the board with the LED's blinking, with the RED LED at the slower speed

so NOT at 7.3 Hz, but at 3.8 Hz

Be sure to say your name, Date, and Time